

Vector Modulator

Technical Report

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1 Introduction

Vector modulators are devices which modulate an input carrier signal according to some modulation scheme. Common schemes include frequency modulation (FM), phase-shift keying (PSK), and quadrature amplitude modulation (QAM). The vector modulator detailed herein encodes data using 16-point quadrature amplitude modulation (16-QAM) [1], [2]. This modulator takes a four bit input and maps each input bit to one of 16 equally spaced points on a constellation diagram. It would be used in a transmitter and would be used to encode a signal.

2 Device Specifications

Specification	Target	Achieved	Margin
Center Freq	5.4 GHz	yes	
Data Rate	810 Msps	yes	
Bandwidth	1.62 GHz	yes	
Maximum IL	9.1 dB	6.2 dB	2.9 dB
Phase Accuracy	+/- 4.5°	+/- 3.9°	0.6°
Amplitude Variation	+/- 0.75 dB	+/- 0.4 dB	0.35 dB
VSWR	1.4	1.29	0.11
DC Power		200 mW	
RF Power		675 mW	
DC Current	per diode	10 mA	
Reverse Voltage	per diode	-10 V	
Size		2.1 x 1.3 in	

Table 1: Design specifications, actual worst-case performance, and margin achieved.

Table 1 details the design specifications (as listed in [3]), the achieved specifications, and the margins over spec. The devices detailed herein is within spec with margin in all cases (including effects of worst-case component tolerances); actual margins are detailed in the table. Some specifications were not given as design targets; in this case, the corresponding target entries were left blank.

2.1 Power and Biasing

Each of the PIN diodes in the device requires 10 mA to switch on. The full schematic, given in appendix A, includes bias networks for each diode made of a single inductor and bias resistor in series. This allows only DC current to flow into the diode and blocks any microwave signals from entering the baseband circuitry. Coupling capacitors on either side of the diode block any DC current from flowing into the rest of the modulator. The DC control voltages are represented by grounds because, from an AC perspective, the DC rails are grounded. Each of these grounds should be replaced by an appropriate bias circuit, according to the information in tables 1 and 2 and the full schematic in appendix A.

State	bit ~3	bit 2	bit 2	bit 1	bit 0	bit ~0
0000	off	off	on	off	off	on
0001	off	off	on	off	on	off
0010	off	off	on	on	off	on
0011	off	off	on	on	on	off
0100	off	on	off	off	off	on
0101	off	on	off	off	on	off
0110	off	on	off	on	off	on
0111	off	on	off	on	on	off
1000	on	off	on	off	off	on
1001	on	off	on	off	on	off
1010	on	off	on	on	off	on
1011	on	off	on	on	on	off
1100	on	on	off	off	off	on
1101	on	on	off	off	on	off
1110	on	on	off	on	off	on
1111	on	on	off	on	on	off

Table 2: Bias settings for each state of the modulator, given in terms of the diode designations. `on` indicates that 10 mA should flow into the designated node for each diode supplied from it, while `off` indicates that the designated node should have -10 V applied to it.

2.2 Constellation Diagram

The ideal constellation diagram for the device is given in figure 1, and the corresponding per-point angles are given in table 3. All amplitudes are

normalized to the maximum output amplitude of the device (the outer four corner states); all angles are relative to state 0000, the upper-right-hand corner, consistent with the general characterization schema already used. The code used to generate this figure and table may be found in appendix B.

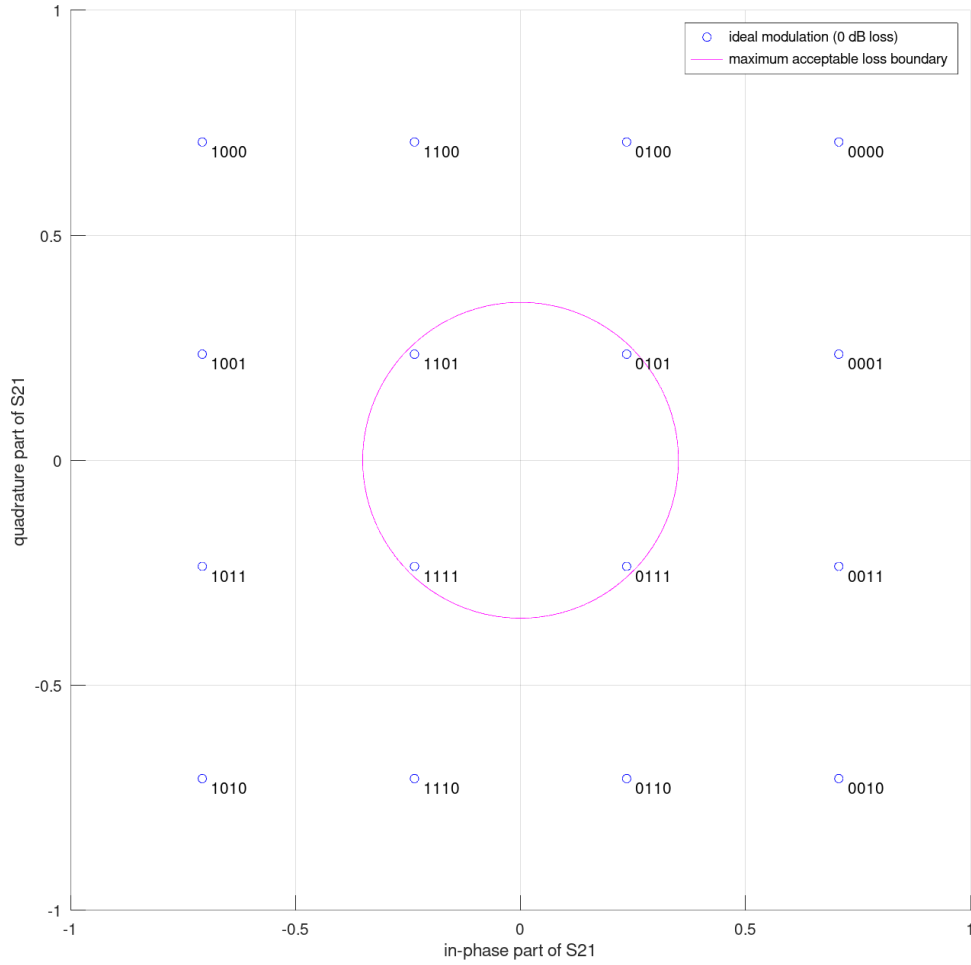


Figure 1: The ideal constellation diagram for the modulator.

State	Amplitude	Phase
0000	0.00 dB	0.0°
0001	-2.55 dB	-26.6°
0010	0.00 dB	-90.0°
0011	-2.55 dB	-63.4°
0100	-2.55 dB	26.6°
0101	-9.54 dB	0.0°
0110	-2.55 dB	-116.6°
0111	-9.54 dB	-90.0°
1000	0.00 dB	90.0°
1001	-2.55 dB	116.6°
1010	0.00 dB	-180.0°
1011	-2.55 dB	-206.6°
1100	-2.55 dB	63.4°
1101	-9.54 dB	90.0°
1110	-2.55 dB	-153.4°
1111	-9.54 dB	-180.0°

Table 3: Ideal amplitude (relative to maximum) and phase for the modulator.

The actual constellation diagram of the device, swept across its 1.62 GHz bandwidth, is given in figure 2. Each state is labeled with its corresponding bit input. Per-state margins, with worst-case states highlighted, are given in table 4. All specifications for both tables are referenced to the center (average complex value) of state 0000. The code used to derive the specifications from raw S-parameter data may be found in appendix C.

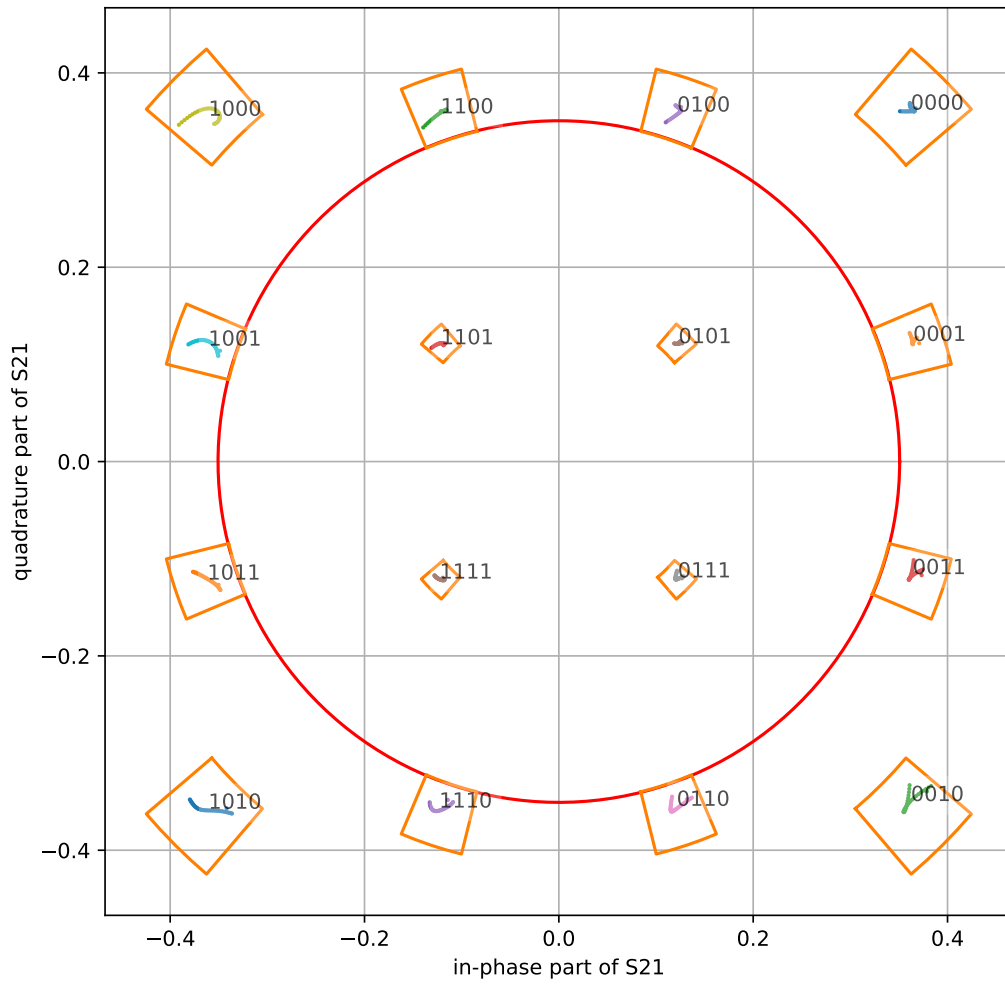


Figure 2: The constellation diagram of the device as built. Each curve represents the movement of a constellation point as frequency is swept over the device's 1.62 GHz bandwidth. The orange annular sections show gain and phase tolerance for each state, and the red circle shows the maximum insertion loss for the outermost states.

State	Gain Margin	Phase Margin	VSWR Margin	IL
0000	0.60 dB	3.74°	0.15	5.97 dB
0001	0.56 dB	2.80°	0.19	8.32 dB
0010	0.39 dB	0.57°	0.24	6.18 dB
0011	0.54 dB	1.59°	0.30	8.43 dB
0100	0.39 dB	3.55°	0.23	8.73 dB
0101	0.49 dB	3.28°	0.28	15.40 dB
0110	0.34 dB	1.35°	0.23	8.78 dB
0111	0.48 dB	2.15°	0.30	15.62 dB
1000	0.46 dB	1.05°	0.26	6.10 dB
1001	0.35 dB	3.29°	0.11	8.71 dB
1010	0.44 dB	1.99°	0.16	6.12 dB
1011	0.49 dB	2.15°	0.20	8.58 dB
1100	0.51 dB	0.84°	0.24	8.61 dB
1101	0.50 dB	1.23°	0.15	15.47 dB
1110	0.40 dB	2.19°	0.23	8.72 dB
1111	0.60 dB	1.97°	0.28	15.44 dB

Table 4: Gain, phase, and VSWR margins (over spec.) per device state, along with the maximum insertion loss for that state. Worst-case states and margins are shown in bold.

3 Block Diagram

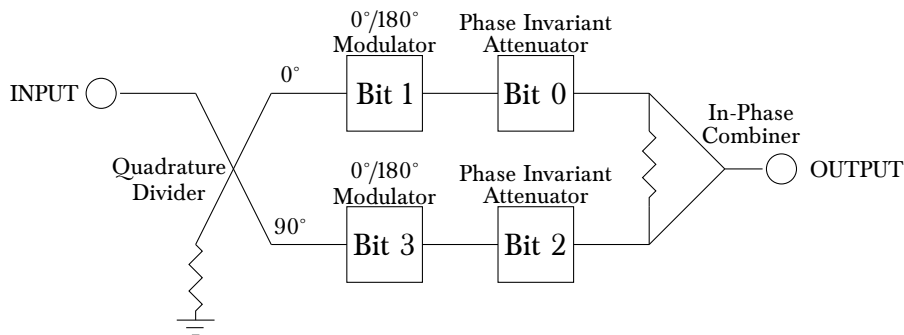


Figure 3: The block diagram for the device.

Figure 3 shows the block diagram of the vector modulator, identical to that given in [2]. The device has two inputs: one for an RF signal, and one (consisting of four single-bit inputs) for the baseband signal. The bits set

at the baseband input determine the state of the device. For the purposes of this discussion, the most significant input bit is bit 3, while the least significant is bit 0.

As depicted in figure 3, the input signal is first passed through a quadrature divider and decomposed into in-phase (I) and quadrature (Q) components. Once it is divided in quadrature, the 4-bit baseband input is used to manipulate it. The quadrant of the constellation diagram is determined first, using bits 3 and 1. When set to 1, bits 3 and 1 determine if the signal's I and Q components, respectively, should be inverted. If they are set to 0, it indicates that the signal should not be inverted. In other words, the quadrature divider and $0^\circ/180^\circ$ modulators determine the quadrant for the input state.

Bits 2 and 0 scale the I and Q components, respectively, by indicating if the component should be attenuated. By setting either of these bits to 1, its respective component is attenuated. If set to 0, then the respective component is not attenuated. Finally, the signal passes through an in-phase combiner, which performs a vector addition of the I and Q components.

4 Implementation

The following sections describe the implementation of the device and the design tradeoffs for each part. The modulator's quadrature divider, $0^\circ/180^\circ$ phase shifter, phase invariant attenuator and in-phase combiner were realized with a dual Lange coupler, reflective phase shifter, dual SPDT PIN diode switches with a T-bridge attenuator, and a Wilkinson combiner, respectively.

4.1 Quadrature Divider

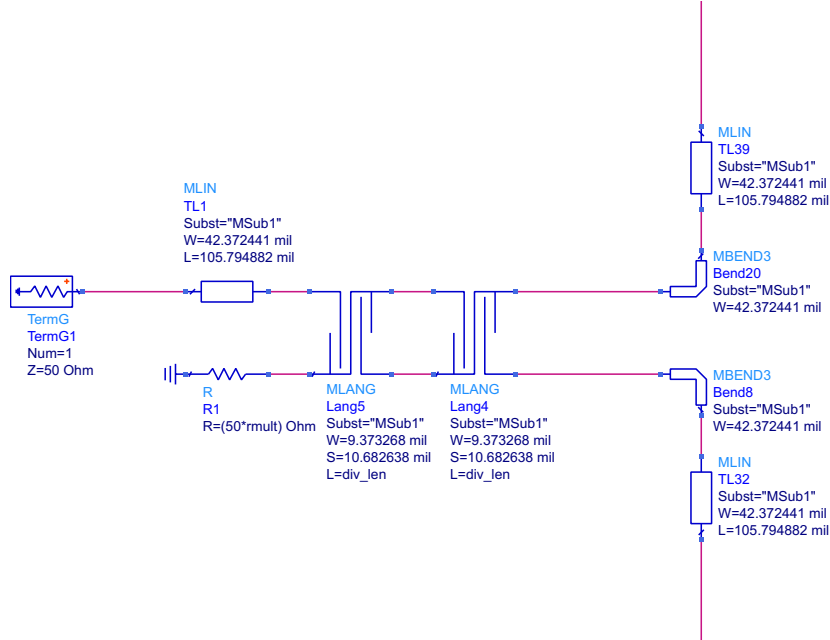


Figure 4: The quadrature divider. The upper branch feeds the in-phase section of the modulator, and the lower half feeds the quadrature section.

As depicted in figure 4, two Lange couplers (analyzed in [4]) are used to create a phase difference of 90° between the two output ports. The resistor R1 is used as a matched termination to ensure that any spurious signal coming out of the isolated port will not be reflected back into the coupler. The bends and transmission lines coming out of the output ports of the coupler are added to the schematic because when actually laying out the design, there would be bits of transmission line connecting the quadrature divider to the following stage.

The overall coupling coefficient of the two couplers is 3 dB, as we want even power across the I and Q signals. Two couplers are used instead of one coupler for two reasons: one, achieving a 3 dB coupling coefficient with one coupler would require a tiny spacing between the coupler's fingers, which is difficult to manufacture; second, two couplers had better phase response for this particular block than a single coupler. With two

couplers, built as depicted in figure 4, the coupling coefficient is 8.34 dB for each coupler. The separation distance between the fingers is thus larger – around 10.68 mils, instead of less than a mil – which is more reasonable to manufacture (especially since the laser beam used to mill out the metal for the coupler has a 1.5 mil diameter).

4.1.1 Coupling Coefficient Derivation

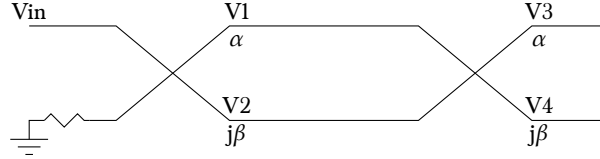


Figure 5: A schematic diagram of a double quadrature coupler like the one employed here. The coefficients near the terminals indicate the S-parameters associated with those terminals.

With reference to figure 5, inputting a voltage wave V_{in} to a double quadrature coupler produces waves

$$V_1 = \alpha V_{in}^+ \text{ and } V_2 = j\beta V_{in}$$

at the outputs of the first coupler. Feeding this into an identical second coupler produces

$$V_3 = \alpha V_1 + j\beta V_2 = \alpha^2 V_{in} + (j\beta)^2 V_{in} = (\alpha^2 - \beta^2) V_{in}$$

$$\text{and } V_4 = j\beta V_1 + \alpha V_2 = j\alpha\beta V_{in} + j\alpha\beta V_{in} = 2j\alpha\beta V_{in}$$

at the output. We require $|S_{31}| = \alpha^2 - \beta^2 = 1/\sqrt{2}$ for 3 dB coupling overall, and set $\alpha^2 + \beta^2 = 1$ for a lossless coupler, implying our coupling coefficient, β , is

$$\beta = \sqrt{\frac{1}{2} \left(1 - \frac{1}{\sqrt{2}} \right)} \approx 0.3827,$$

or about -8.34 dB.

4.1.2 Alternatives

One alternative to the dual Lange coupler which was considered was a quadrature ring hybrid. This device was implemented in ADS, but proved

to have unsatisfactory phase and amplitude response across our bandwidth. Figure 6 shows the phase response of the coupler, which would immediately put the device out of spec if used. The Lange coupler, with its cancellation of even- and odd-mode harmonics, proved a better candidate, as shown in figure 7.

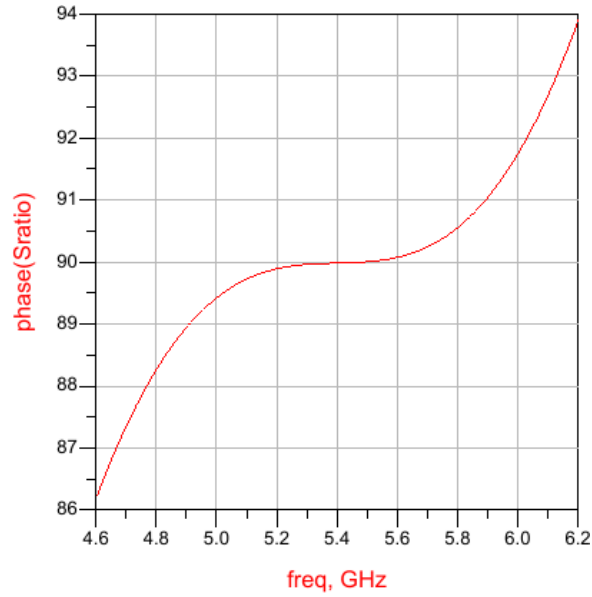


Figure 6: The (unsatisfactory) phase response of a quadrature ring hybrid.

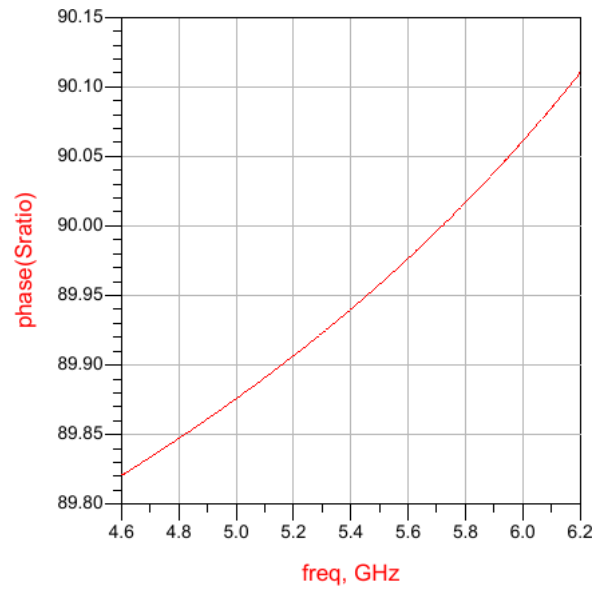


Figure 7: The phase response of our Lange coupler.

Another option that was considered to realize the divider was purchasing a quadrature divider in the market. However, this device would not be designed to having minimum cost. It was also difficult to find a coupler that would operate within the bandwidth and not exceed more than 3 dB attenuation.

4.2 0°/180° Modulator

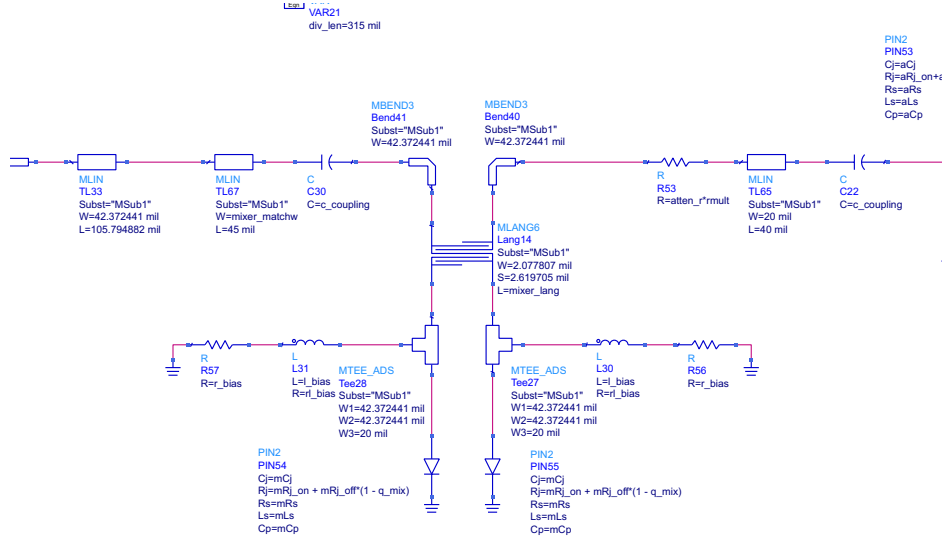


Figure 8: The 0°/180° modulator. Depending on the state of the diodes, the input signal reflects from them with a reflection coefficient of +1 or -1.

The modulator is realized with a 180° reflective phase shifter, depicted in figure 8. The signal enters through the left side of the device through the transmission lines and passes through the 6-finger Lange coupler. Depending on the state of the PIN diodes (on or off), the signal either sees a short or an open circuit at the output of the coupler. Therefore, there is complete reflection with a reflection coefficient of either +1 or -1. If the diodes are on, the signal sees a short and is reflected with inversion. Otherwise the signal is reflected back with no inversion. The coupler is used to ensure that the reflected signal comes out of a separate output port and not back out of the input port. (A circulator would also work, but would be larger and far more expensive.) Essentially, this phase shifter behaves like a mixer with a second input signal of either a logical +1 or -1.

4.2.1 Alternatives

It is also possible to realize this modulator with a 180° ring-hybrid modulator as depicted in figure 9. This structure would send the signal through port 1 and depending on the diode that is on, the signal will either have a 90° phase shift or 270° degree phase shift. Hence, there is a 180° phase difference between the two paths. While this design functionally works, it does not achieve a VSWR that would be within the target specifications. Also, the insertion loss from this device is too large when there is a 90° phase shift from the input signal, also depicted in figure 9. The attenuation is anywhere in the range of -8.6 dB to -7.4 dB, which is too much for one stage of the device and far too variable to meet our specifications. Hence, this structure is not the best to implement in comparison to the reflective phase shifter.

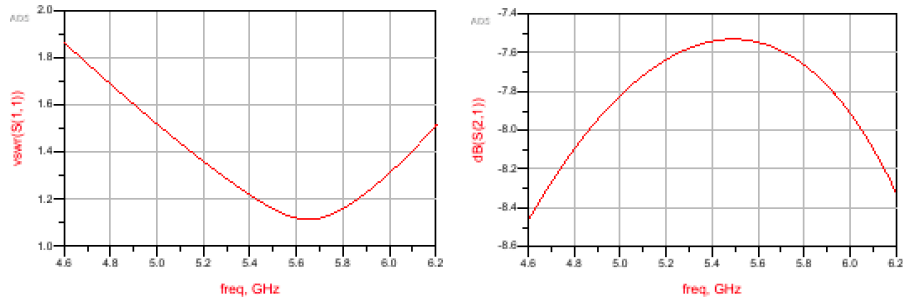
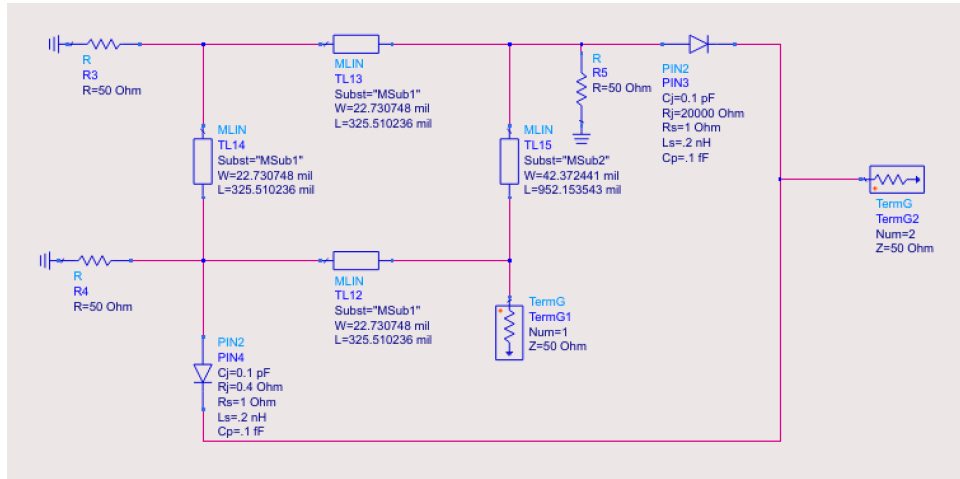


Figure 9: Possible $0^\circ/180^\circ$ design using 180° ring hybrid structure. This structure was not implemented due to unsatisfactory VSWR and insertion loss.

Another alternative would be a double-balanced mixer. However, such a device would be complex to implement on an already tight board. Since the reflective shifter above worked for our application, such complexity is not necessary. Yet another alternative would be a switched-line phase shifter built with an all-pass filter. Such a phase shifter could be an attractive alternative, if modifications (size, parts, etc.) to the design need be made.

4.3 Phase Invariant Attenuator

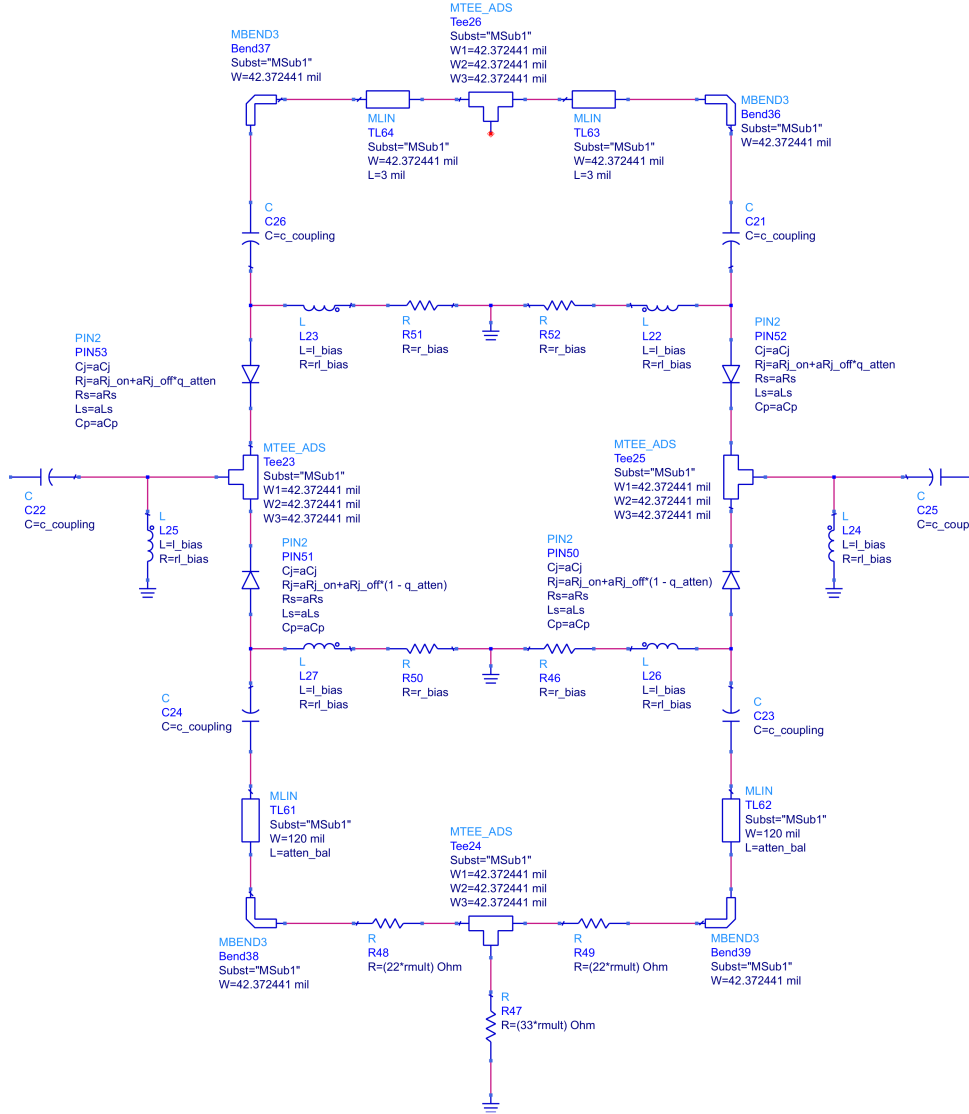


Figure 10: The phase invariant attenuator. The upper branch provides near-zero attenuation, while the lower branch provides about 9.54 dB of attenuation.

The attenuator, depicted in figure 10, consists of a SPDT pin diode switch set to select one of two possible signal routes. The upper branch cor-

responds to the path of no attenuation and the lower branch allows for attenuation.

To enable attenuation, the lower branch's PIN diodes are both switched on and the upper branch's diodes are switched off, routing the signal through a T attenuator. Design equations for this attenuator were obtained from [4], and design inspiration was taken from [5]. To disable attenuation, the lower branch's PIN diodes are switched off and the upper branch's diodes are switched on, routing the signal through a passthrough line. Two coupling capacitors are used in the upper branch, rather than none, to help maintain the symmetry of the device; similarly, an unused T connection is placed in the upper branch, to aid in making the upper branch's shape similar to the lower branch to keep matching consistent.

4.3.1 Alternatives

Another way to implement this attenuator was to use the structure presented in figure 11. However as seen in the VSWR curve across the bandwidth, the VSWR is always larger than the requirement. It is between 2.015 to 2.050 which is much higher than 1.4. With the diode off, the match is much better, but in order to meet spec with this design, the diode would *also* have to switch a matching network on.

By incorporating SPDT switches into the attenuator, the attenuated and passthrough signal paths are far more symmetrical, and any matching network used applies more fully to both of them. Hence, while the design that is proposed in figure 11 would be simpler to manufacture, it does not meet the target specification and was not used.

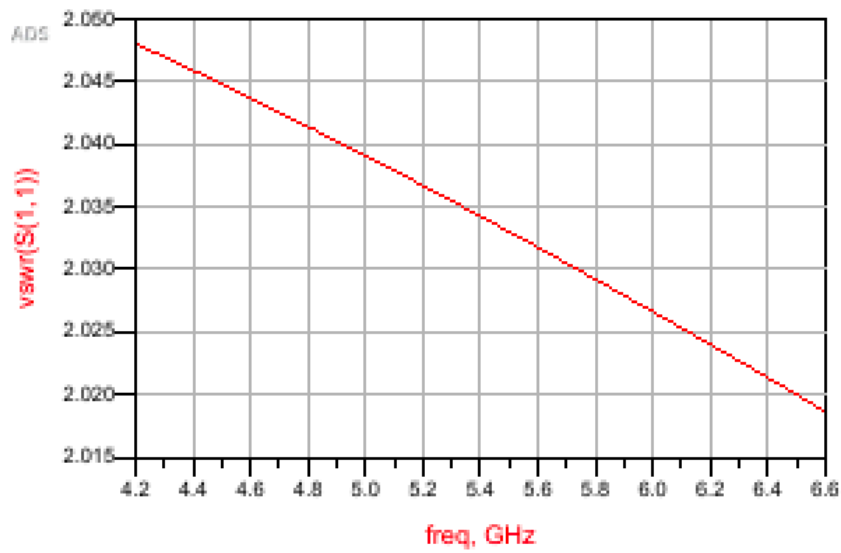
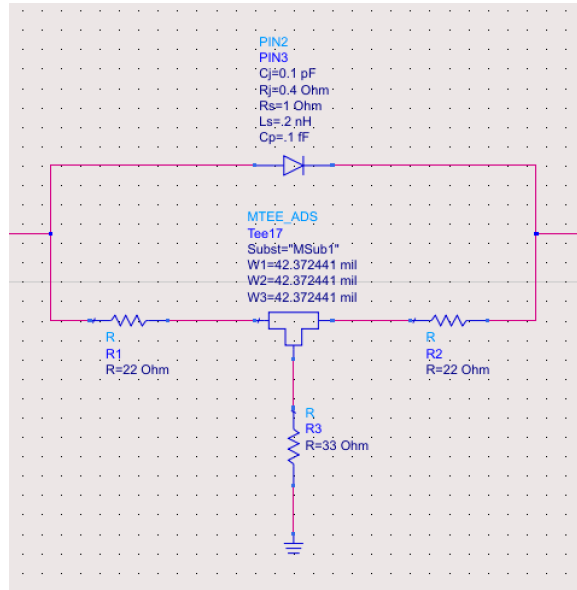


Figure 11: Possible option to implement the phase invariant attenuator, not implemented due to its poor matching characteristics and lack of symmetry.

4.4 In-phase Combiner

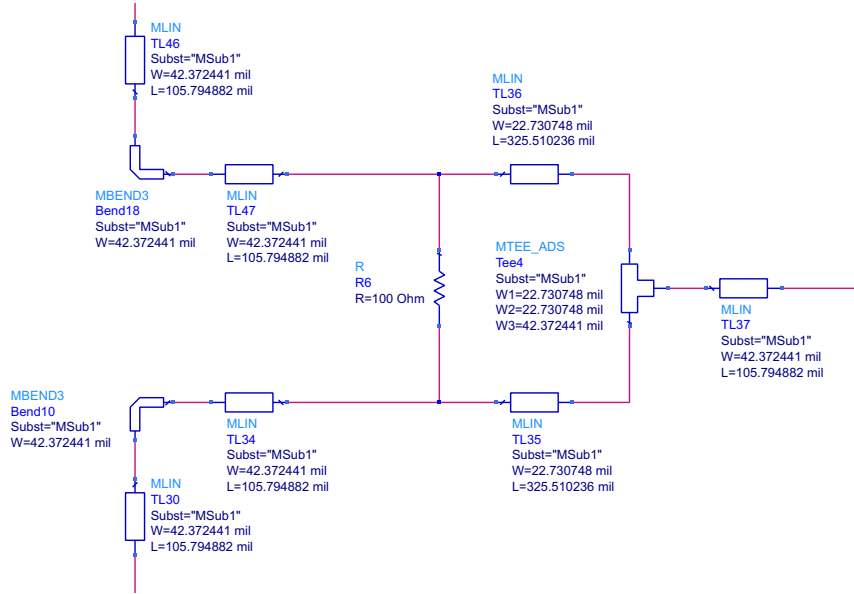


Figure 12: The in-phase combiner, implemented with a Wilkinson vectorial combiner.

The in-phase combiner is realized with a Wilkinson combiner (analyzed in [4]), shown in figure 12. The in-phase component in the upper branch and the quadrature component in the lower branch sum up from TL36 and TL35 into Tee4 and TL37. Due to the transmission lines TL35 and TL36 being designed to quarter-wavelength, there is no addition across the resistor R6 because there would be ideally a 180° phase shift in the signal and effectively no change across the resistors, so it appears that the two input ports are isolated from each other. In this structure, there are additional transmission lines feeding into the combiner and out of it to make it more realistic when laying out this device. Since the Wilkinson combiner is the simplest and most straightforward way to implement this in-phase combiner, no other methods were considered. While it would be possible to also buy a combiner, it would be too expensive.

5 Parts

Manufact.	Part No.	Value	Count	Cost
Vishay	FC0805K22R0JBTS	22 R	4	\$0.84
Vishay	FC0805K33R0JBTS	33 R	2	\$0.10
Vishay	FC0805K50R0JBTS	50 R	1	\$0.12
Vishay	FC0805K1000JBTS	100 R	1	\$2.35
Kyocera	04023K1R7CBWTR	1.7 pF	28	\$0.65
Kyocera	HLQ026R8BTTR	6.8 nH	32	\$0.21
Macom	MADP-000907-14020x	PIN diode	16	\$4.31
Total Cost (parts only)				\$99.91

Table 5: Discrete parts used in the design.

Table 5 gives a breakdown of the discrete parts used for the device, along with their costs. Actual fabrication costs will be higher, owing to board manufacturing and assembly.

6 Device Considerations

6.1 Size

Block Diagram Part	Size		
Quadrature Divider	401.7 mils	x	430.06 mils
0°/180° Modulator	560 mils	x	195.243 mils
Phase Invariant Attenuator	582.38 mils	x	424.23 mils
In-Phase Combiner	403.64 mils	x	553.36 mils
Total Size	2097.49 mils	x	1278.48 mils

Table 6: Size of each block in block diagram.

One of the design goals for this device was to minimize its size. Table 6 lists the size of each component in the block diagram along with the total size, where the total size refers to the size of all parts arranged similarly to the schematic in appendix A.

6.2 DC Power

Block Diagram Part	Average DC Power
Quadrature Divider	0 mW
0°/180° Modulator	100 mW
Phase Invariant Attenuator	100 mW
In-Phase Combiner	0 mW
Total	200 mW

Table 7: Average DC power consumption of each block in block diagram.

Another design goal was to achieve minimum power consumption. Table 7 lists the average DC power consumed for each block in the block diagram and the total power of the whole device put together. Since the quadrature divider and in-phase combiner do not need to be turned on or off, there are no biasing networks for these parts so they do not absorb any DC power.

This modulator uses 100 Ω bias resistors, which drop 1 V at 10 mA, and each diode consumes 10 mA with a 1.5 V drop. On average, one half of each attenuator and one mixer will be switched on, yielding a total power of

$$(8 \text{ diodes}) (10 \text{ mA}) (1.5 \text{ V} + 2.5 \text{ V}) = 200 \text{ mW}.$$

6.3 RF Power

Total RF power handling is limited by the attenuator resistors, each rated for 200 mW. About 2/3 of the power is dissipated in the first resistor, due to even current division across the second two with an incident voltage signal. To reduce the signal to 1/3 of its original amplitude, the attenuators need to dissipate $1 - (1/3)^2 = 8/9$ of the signal power. Half the dissipated power is lost per attenuator. Thus, the maximum RF power handling of the device is

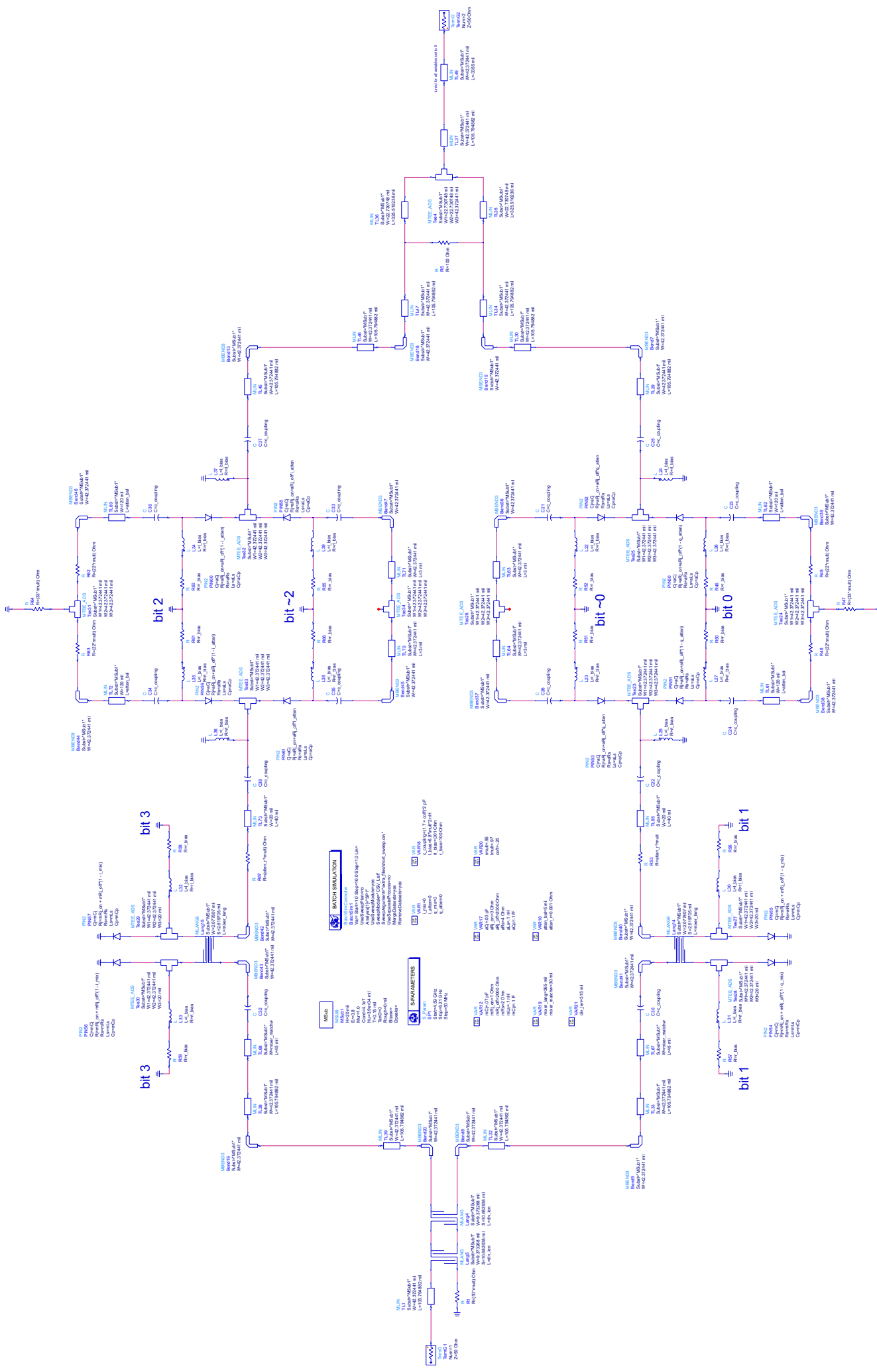
$$\frac{2 \cdot 200 \text{ mW}}{\frac{8}{9} \cdot \frac{2}{3}} = 675 \text{ mW}.$$

7 References

- [1] *Understanding Constellation Diagrams and How They Are Used*, Application Note AN-005, NuWaves Engineering.
- [2] *I-Q Vector Modulator – The Ideal Control Component!* (from Theory of Operation & Practical Applications), General Microwave.
- [3] L. Hausman, *ECE 431 Vector Modulator Design Specification*, 2023.
- [4] D. M. Pozar, *Microwave Engineering*, 4th ed. John Wiley & Sons, Inc., 1998.
- [5] K. Blattenberger, *Fixed Attenuators*, RF Cafe, 2010. [Online]. Available: <http://home.sandiego.edu/~ekim/e194rfs01/attenuators/attenuators.html>.

All schematics drawn and simulated with Keysight ADS.

Appendix A Full Schematic



BATCH SIMULATION

SPICE
 ANSYS
 MATLAB/SIMULINK
 THERMAL
 MECHANICAL
 FLUID DYNAMICS
 OPTICS
 ELECTROMAGNETICS
 SIGNAL INTEGRITY
 POWER INTEGRITY
 INTERFERENCE
 ESD
 RELIABILITY
 MANUFACTURING
 COST ANALYSIS
 SUPPLY CHAIN
 ENVIRONMENTAL
 SAFETY
 COMPLIANCE
 CUSTOMER EXPERIENCE
 PROJECT MANAGEMENT
 RISK MANAGEMENT
 SUSTAINABILITY
 INNOVATION
 TALENT DEVELOPMENT
 ORGANIZATIONAL CULTURE
 CHANGE MANAGEMENT
 STRATEGIC PLANNING
 FINANCIAL ANALYSIS
 MARKETING
 SALES
 CUSTOMER SUPPORT
 LOGISTICS
 LEGAL
 HUMAN RESOURCES
 IT SUPPORT
 FACILITIES
 OTHER

PARAMETERS

R1: 100 Ohm
 R2: 100 Ohm
 R3: 100 Ohm
 R4: 100 Ohm
 R5: 100 Ohm
 R6: 100 Ohm
 R7: 100 Ohm
 R8: 100 Ohm
 R9: 100 Ohm
 R10: 100 Ohm
 R11: 100 Ohm
 R12: 100 Ohm
 R13: 100 Ohm
 R14: 100 Ohm
 R15: 100 Ohm
 R16: 100 Ohm
 R17: 100 Ohm
 R18: 100 Ohm
 R19: 100 Ohm
 R20: 100 Ohm
 R21: 100 Ohm
 R22: 100 Ohm
 R23: 100 Ohm
 R24: 100 Ohm
 R25: 100 Ohm
 R26: 100 Ohm
 R27: 100 Ohm
 R28: 100 Ohm
 R29: 100 Ohm
 R30: 100 Ohm
 R31: 100 Ohm
 R32: 100 Ohm
 R33: 100 Ohm
 R34: 100 Ohm
 R35: 100 Ohm
 R36: 100 Ohm
 R37: 100 Ohm
 R38: 100 Ohm
 R39: 100 Ohm
 R40: 100 Ohm
 R41: 100 Ohm
 R42: 100 Ohm
 R43: 100 Ohm
 R44: 100 Ohm
 R45: 100 Ohm
 R46: 100 Ohm
 R47: 100 Ohm
 R48: 100 Ohm
 R49: 100 Ohm
 R50: 100 Ohm
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 R73: 100 Ohm
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 R77: 100 Ohm
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 R79: 100 Ohm
 R80: 100 Ohm
 R81: 100 Ohm
 R82: 100 Ohm
 R83: 100 Ohm
 R84: 100 Ohm
 R85: 100 Ohm
 R86: 100 Ohm
 R87: 100 Ohm
 R88: 100 Ohm
 R89: 100 Ohm
 R90: 100 Ohm
 R91: 100 Ohm
 R92: 100 Ohm
 R93: 100 Ohm
 R94: 100 Ohm
 R95: 100 Ohm
 R96: 100 Ohm
 R97: 100 Ohm
 R98: 100 Ohm
 R99: 100 Ohm
 R100: 100 Ohm

Appendix B Geometry Code

```

1  %%% Constellation Point Calculator
2  % Haley Dave & Cat Van West, Autumn `23
3  close all; clear; clc;
4
5  % defining here, just to be sure it's voltage decibels
6  db2volt = @(x) 10.^(x ./ 20);
7  volt2db = @(x) 20.*log10(x);
8
9  insertion_loss = -15; % in dB
10 max_acceptable_loss = -9.1; % also in dB
11
12 x = linspace(-sqrt(2)/2, sqrt(2)/2, 4);
13 y = j*fliplr(x).';
14 ideal_points = x + y; % broadcast out to a square
15 %refactored_points = db2volt(insertion_loss)*ideal_points;
16
17 point_labels = [1000, 1100, 0100, 0000] + [0000; 0001; 0011; 0010];
18
19 % absurd one-liner that MATLAB won't let you do!
20 %max_loss_circle = db2volt(max_acceptable_loss) ...
21 %      *(@(x) [cos(x); sin(x)])(linspace(0, 2*pi, 1000));
22 circle_angles = linspace(0, 2*pi, 1000);
23 unit_circle = [cos(circle_angles); sin(circle_angles)];
24 max_loss_circle = db2volt(max_acceptable_loss)*unit_circle;
25
26 iscatter = @(pts, c) scatter(real(pts), imag(pts), 30, c);
27
28 figure;
29 hold on;
30 grid on;
31 iscatter(ideal_points, 'b');
32 %iscatter(refactored_points, 'r');
33 plot(max_loss_circle(1, :), max_loss_circle(2, :), 'm');
34 xlabel("in-phase part of S21");
35 ylabel("quadrature part of S21");
36 daspect([1 1 1]);
37
38 % whack down point labels
39 disp("state, mag, deg:");
40 offset = .02;

```

```

41 for i = 1:16
42     point = ideal_points(i);
43     state = point_labels(i);
44     text(...
45         real(point) + offset, ...
46         imag(point) - offset, ...
47         sprintf("%04d", state) ...
48     );
49     fprintf(...
50         [...
51             "          %04d & \\verb`%5.2f` dB" ...
52             " & \\verb`%6.1f`\\textdegree \\|\\|\\|n" ...
53         ], ...
54         state, volt2db(abs(point)), rad2deg(angle(point)) - 45 ...
55     );
56 end
57
58 legend(...
59     "ideal modulation (0 dB loss)", ...
60     %     sprintf("insertion loss of %d dB", insertion_loss), ...
61     "maximum acceptable loss boundary" ...
62 );
63
64 % print out dB for attenuator
65 fprintf("attenuator dB: %d\n", volt2db(x(2)/x(1)));
66
67 % work out resistor values for an unbalanced T attenuator
68 atten_ratio_power = (x(1)/x(2))^2;
69 Z0 = 42;
70 R1R2_val = @(Z0, K) Z0*(sqrt(K) - 1)/(sqrt(K) + 1);
71 R3_val = @(Z0, K) 2*Z0*sqrt(K)/(K - 1);
72
73 R1R2 = R1R2_val(Z0, atten_ratio_power);
74 R3 = R3_val(Z0, atten_ratio_power);
75 fprintf("resistor values: R1, R2 = %d Ω, R3 = %d Ω\n", R1R2, R3);

```

Appendix C Analysis Code

```

1  #!/usr/bin/env python3
2  # -*- coding: utf-8 -*-
3  """
4  Created on Wed Nov 22 13:09:28 2023
5
6  @author: Catherine Van West
7  @date: Nov 2023
8  """
9
10 import ads
11
12 import matplotlib.pyplot as plt
13 import numpy as np
14 import numpy.polynomial.polynomial as poly
15 import pandas as pd
16
17 import pathlib
18
19 # functions for describing what we're up to
20 def specifications():
21     return {
22         'center frequency': 5.4e9, # Hz
23         'symbols per second': 810e6,
24         'bandwidth': 1.62e9, # Hz
25         'insertion loss': -9.1, # dB
26         'phase accuracy': 4.5, # ±, °
27         'vswr': 1.4,
28         'impedance': 50, # Ω
29         'amplitude variation': .75, # ±, dB, vs. freq.
30     }
31
32 def read_sweep(filename):
33     return pd.read_csv(pathlib.Path('../extra_files') / filename)
34
35 # useful functions
36 def db2volt(x):
37     return 10 ** (x / 20)
38
39 def volt2db(x):
40     return 20*np.log10(x)

```

```

41
42 def isscatter(ax, pts):
43     ax.scatter(np.real(pts), np.imag(pts), s=3)
44
45 # workhorse functions
46 def reference_constellation(insertion_loss=0):
47     x = np.sqrt(2)/2 * np.linspace(-1, 1, 4)
48     y = 1j*np.reshape(x, (-1, 1))
49     ideal_points = x + y
50     refactored_points = db2volt(insertion_loss) * ideal_points
51     return refactored_points
52
53 def unit_circle():
54     angles = np.linspace(0, 2*np.pi, 1000)
55     return np.vstack((np.cos(angles), np.sin(angles)))
56
57 def nth_sweep(S21, n):
58     return S21[S21[:, 0] == n, 2]
59
60 def first_sweep(S21):
61     return nth_sweep(S21, 1)
62
63 def delinearize(S21, rotate_square=True):
64     first = first_sweep(S21)
65
66     # do a linear fit to the angles of the first sweep
67     pts = np.linspace(0, 1, first.size)
68     angle_fit = poly.Polynomial.fit(
69         pts, np.unwrap(np.angle(first)), 1
70     )
71
72     # subtract those angles out and return
73     angle_sub_factor = np.exp(-1j*angle_fit(pts))
74
75     if rotate_square:
76         angle_sub_factor = angle_sub_factor*np.exp(1j*np.pi/4)
77
78     sweep_count = int(np rint(np.max(np.real(S21[:, 0])))
79     S21[:, 2] = S21[:, 2]*np.tile(angle_sub_factor, sweep_count)
80

```

```

81     return S21
82
83 def centered_constellation(S21):
84     # assumptions: the first sweep sits in an outer corner of the
85     # constellation; all the point locations will be computed from there
86     first_midpoint = np.mean(first_sweep(S21))
87
88     # rotate and squash the reference so it lines up with the corner
89     reference = reference_constellation()
90     squash_factor = first_midpoint/reference[0, 0];
91     constellation = np.reshape(reference*squash_factor, (-1, 1));
92
93     return constellation
94
95 def device_state(sweep, index):
96     return ''.join(map(str, sweep.iloc[index, :]))
97
98 def error_over_sweep_pts(S21, f):
99     ideal = centered_constellation(S21)
100    errors = []
101
102    for sweep_index in range(int(np.max(S21[:, 0]))):
103        sweep_errors = []
104
105        for point_index in range(16):
106            error = np.max(f(
107                nth_sweep(S21, sweep_index + 1), ideal[point_index]
108            ))
109            sweep_errors.append(error)
110
111        errors.append(np.min(sweep_errors))
112
113    return errors
114
115 def gain_errors_db(S21):
116     def f(sweep, pt):
117         return np.abs(volt2db(np.abs(sweep / pt)))
118
119     return error_over_sweep_pts(S21, f)
120

```

```

121 def phase_errors_deg(S21):
122     def f(sweep, pt):
123         return np.rad2deg(np.abs(np.angle(sweep / pt)))
124
125     return error_over_sweep_pts(S21, f)
126
127 def vswr(S11):
128     vswr_sweeps = []
129
130     for sweep_index in range(int(np.max(S11[:, 0]))):
131         abs_gamma = np.abs(nth_sweep(S11, sweep_index + 1))
132         vswr_sweep = (1 + abs_gamma)/(1 - abs_gamma)
133         vswr_sweeps.append(np.max(vswr_sweep))
134
135     return vswr_sweeps
136
137 def insertion_loss_db(S21):
138     il = []
139
140     for sweep_index in range(int(np.max(S11[:, 0]))):
141         il_sweep = -volt2db(np.abs(nth_sweep(S21, sweep_index + 1)))
142         il.append(np.max(il_sweep))
143
144     return il
145
146 def max_gain_error_db(S21):
147     errors = gain_errors_db(S21)
148     return np.max(errors), np.argmax(errors)
149
150 def max_phase_error_deg(S21):
151     errors = phase_errors_deg(S21)
152     return np.max(errors), np.argmax(errors)
153
154 def find_margins(sweep, max_spec, errors):
155     states, margins = [], []
156
157     for i in range(len(errors)):
158         states.append(device_state(sweep, i))
159         margins.append(max_spec - errors[i])
160

```

```

161     return states, margins
162
163 def gain_margins(spec, S21, sweep):
164     return find_margins(
165         sweep,
166         spec['amplitude variation'],
167         gain_errors_db(S21)
168     )
169
170 def phase_margins(spec, S21, sweep):
171     return find_margins(
172         sweep,
173         spec['phase accuracy'],
174         phase_errors_deg(S21)
175     )
176
177 def vswr_margins(spec, S11, sweep):
178     return find_margins(
179         sweep,
180         spec['vswr'],
181         vswr(S11)
182     )
183
184 def save_modulator_table(spec, S11, S21, sweep, filename):
185     states, gain_m = gain_margins(spec, S21, sweep)
186     _, phase_m = phase_margins(spec, S21, sweep)
187     _, vswr_m = vswr_margins(spec, S11, sweep)
188     il_db = insertion_loss_db(S21)
189
190     ind = ' ' * 8
191     lines = [
192         f'{ind}\\textrm{{State}}' + ''.join(
193             [f' & \\textrm{{s} Margin}}' for s in [
194                 'Gain', 'Phase', 'VSWR',
195             ])
196         ) + f' & \\textrm{{IL}} \\\\n'
197     ]
198
199     min_gain_state = np.argmin(gain_m)
200     min_phase_state = np.argmin(phase_m)

```

```

201     max_vswr_state = np.argmin(vswr_m)
202
203     def _bold(s):
204         return f'\\textbf{{{s}}}'
205
206     for state in range(len(states)):
207         state_str = states[state]
208         gain_str = f'{gain_m[state]:.2f} dB'
209         phase_str = f'{phase_m[state]:.2f}\\textdegree'
210         vswr_str = f'{vswr_m[state]:.2f}'
211         il_str = f'{{{il_db[state]:5.2f}}} dB'
212
213         if state == min_gain_state:
214             state_str = _bold(state_str)
215             gain_str = _bold(gain_str)
216
217         if state == min_phase_state:
218             state_str = _bold(state_str)
219             phase_str = _bold(phase_str)
220
221         if state == max_vswr_state:
222             state_str = _bold(state_str)
223             vswr_str = _bold(vswr_str)
224
225         lines.append(
226             f'{ind}{state_str}'
227             f' & {gain_str}'
228             f' & {phase_str}'
229             f' & {vswr_str}'
230             f' & {il_str}'
231             ' \\\\n'
232         )
233
234     with open(filename, 'w') as file:
235         file.write('').join(lines)
236
237     # plotting functions
238     def plot_reference(ax, spec):
239         c = reference_constellation(spec['insertion loss'] + 1)
240

```

```

241     ax.scatter(
242         np.real(c), np.imag(c), s=3,
243         label='constellation points'
244     )
245
246     def plot_max_loss(ax, spec):
247         max_loss_circle = db2volt(spec['insertion loss'])*unit_circle()
248
249         ax.plot(
250             max_loss_circle[0, :], max_loss_circle[1, :], 'r',
251             label='max loss'
252         )
253
254     def plot_S21(ax, S21, sweep):
255         for sweep_index in range(sweep.shape[0]):
256             this_sweep = S21[:, 0] == sweep_index + 1
257             points = S21[this_sweep, 2]
258             state = device_state(sweep, sweep_index)
259
260             ax.scatter(
261                 np.real(points), np.imag(points), s=1,
262                 # label=state
263             )
264
265             # add a text annotation for state visibility
266             text_pos = np.mean(points)
267             ax.text(
268                 np.real(text_pos), np.imag(text_pos), state,
269                 color='#000000b0', backgroundcolor='#ffffff40',
270             )
271
272     def plot_tolerances(ax, spec, S21):
273         amplitude_factor = db2volt(spec['amplitude variation'])
274         phase_factor = np.deg2rad(spec['phase accuracy'])
275
276         # we'll reference the first point for this
277         midpoints = centered_constellation(S21)
278
279         # plot some annuli
280         points_per_side = 5

```

```

281
282     start_amp = 1/amplitude_factor
283     end_amp = amplitude_factor
284     start_ph = -phase_factor
285     end_ph = phase_factor
286
287     amp_space = np.linspace(start_amp, end_amp, points_per_side)
288     ph_space = np.exp(1j*np.linspace(start_ph, end_ph, points_per_side))
289
290     sides = np.hstack((
291         amp_space*np.exp(1j*start_ph),
292         ph_space*end_amp,
293         np.flip(amp_space, axis=0)*np.exp(1j*end_ph),
294         np.flip(ph_space, axis=0)*start_amp,
295     ))
296
297     annuli = sides * midpoints
298
299     for i in range(annuli.shape[0]):
300         sect = annuli[i, :]
301         ax.plot(np.real(sect), np.imag(sect), '#ff7f00')
302
303     # ax.scatter(
304     #     np.real(midpoints), np.imag(midpoints), s=5,
305     # )
306
307     def plot_text_margins(ax, spec, S21, sweep):
308         gain_error, gain_index = max_gain_error_db(S21)
309         phase_error, phase_index = max_phase_error_deg(S21)
310
311         gain_margin = spec['amplitude variation'] - gain_error
312         phase_margin = spec['phase accuracy'] - phase_error
313
314         gain_state = device_state(sweep, gain_index)
315         phase_state = device_state(sweep, phase_index)
316
317         info_text = (
318             f'margins:\n'
319             f'   gain: {gain_margin:.2f} dB ({gain_state})\n'
320             f'   phase: {phase_margin:.1f} deg. ({phase_state})\n'

```

```

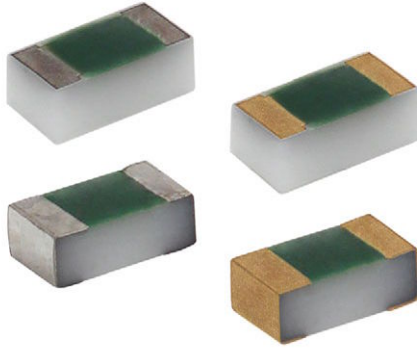
321     )
322
323     ax.text(
324         -.1, -.05, info_text,
325         color='#000000b0', backgroundcolor='#ffffff40'
326     )
327
328     def finish_axes(ax):
329         ax.grid(True)
330         ax.set_aspect('equal')
331         # ax.legend(loc='upper right')
332
333         # ticks = np.linspace(-.3, .3, 7)
334         # ax.set_xticks(ax.get_yticks)
335         # ax.set_yticks(ticks)
336
337         ax.set_xlabel('in-phase part of S21')
338         ax.set_ylabel('quadrature part of S21')
339
340     if __name__ == "__main__":
341         # get our specs
342         spec = specifications()
343
344         # read data from ADS and split it
345         d, s = ads.get()
346         S11, S21 = ads.sep_vars(d)
347
348         # remove linear angle change (allowed)
349         S21_delin = delinearize(S21)
350
351         # read in our current sweep spec
352         sweep = read_sweep(s[0])
353
354         # make a test constellation
355         constellation = reference_constellation(spec['insertion loss'] + 1)
356
357         # create a plot
358         fig, ax = plt.subplots(figsize=(8, 8))
359
360         plot_max_loss(ax, spec)

```

```
361 # plot_reference(ax, spec)
362 plot_S21(ax, S21_delin, sweep)
363 plot_tolerances(ax, spec, S21_delin)
364 # plot_text_margins(ax, spec, S21_delin, sweep)
365 finish_axes(ax)
366
367 # ax.set_title('constellation diagram for the modulator')
368 # plt.show()
369 plt.savefig('constellation.pdf')
370
371 # save information about the states
372 save_modulator_table(spec, S11, S21_delin, sweep, 'character.txt')
373
374 # send plotted data back to ADS
375 ads.send(S21_delin)
```

Appendix D Datasheets

High Frequency (Up to 40 GHz) Resistor, Thin Film Surface Mount Chip



LINKS TO ADDITIONAL RESOURCES



FC series chip resistors are designed with low internal reactance. They function as almost pure resistors on a very high range of frequencies. The specialized laser edge trimming allows for precision tolerances to 0.1 %.

FEATURES

- Small standard size 0402 case size
- Edge trimmed block resistors
- High purity alumina substrate
- Ohmic range (10 Ω to 1000 Ω)
- Small internal reactance (< 10 m Ω)
- Low TCR (down to ± 25 ppm/ $^{\circ}$ C)
- Epoxy bondable termination available
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS*
Available

**HALOGEN
FREE**
Available

**GREEN
(5-2008)**
Available

Note

* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details

APPLICATIONS

- Low noise amplifiers
- Attenuation
- Line termination

STANDARD ELECTRICAL SPECIFICATIONS		
TEST	SPECIFICATIONS	CONDITIONS
Material	Passivated nichrome	-
Resistance Range	10 Ω to 1000 Ω	Case size dependent
TCR: Absolute	± 25 ppm/ $^{\circ}$ C to ± 100 ppm/ $^{\circ}$ C	-55 $^{\circ}$ C to +125 $^{\circ}$ C
Tolerance: Absolute	± 0.1 % to ± 5.0 %	+25 $^{\circ}$ C
Stability: Absolute	$\Delta R \pm 0.02$ %	2000 h at 70 $^{\circ}$ C
Stability: Ratio	-	-
Voltage Coefficient	0.1 ppm/V	-
Working Voltage	30 V to 75 V	-
Operating Temperature Range	-55 $^{\circ}$ C to +155 $^{\circ}$ C	-
Storage Temperature Range	-55 $^{\circ}$ C to +155 $^{\circ}$ C	-
Noise	< -35 dB	-
Shelf Life Stability: Absolute	$\Delta R \pm 0.01$ %	1 year at +25 $^{\circ}$ C

COMPONENT RATINGS			
CASE SIZE	POWER RATING (mW)	WORKING VOLTAGE (V)	RESISTANCE RANGE (Ω)
0402	50	30	10 to 1000
0505	125	37	20 to 1000
0603	125	50	10 to 1000
0805	200	50	10 to 1000
1005	250	75	10 to 1000
1206	330	75	10 to 1000



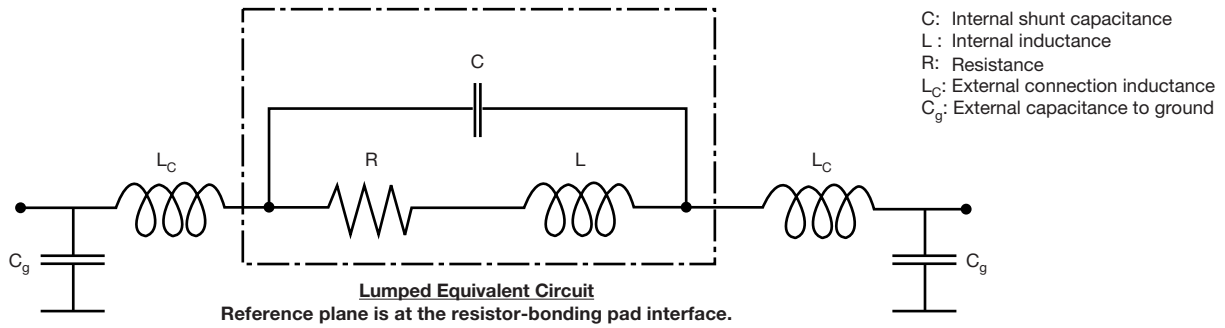
DIMENSIONS in inches (millimeters)						
CASE SIZE	LENGTH	WIDTH W (± 0.005)	THICKNESS T (± 0.0015)	TOP PAD D (± 0.005)	BOTTOM PAD E (± 0.005)	
0402	0.042 ± 0.008 (1.067 ± 0.203)	0.022 (0.559)	0.015 (0.381)	0.010 (0.254)	0.010 (0.254)	
0505	0.055 ± 0.006 (1.397 ± 0.152)	0.050 (1.270)	0.015 (0.381)	0.010 (0.254)	0.015 (0.381)	
0603	0.064 ± 0.006 (1.626 ± 0.152)	0.032 (0.813)	0.015 (0.381)	0.012 (0.305)	0.015 (0.381)	
0805	0.080 ± 0.006 (2.032 ± 0.152)	0.050 (1.270)	0.015 (0.381)	0.016 ± 0.008 (0.406 ± 0.203)	0.015 (0.381)	
1005	0.105 ± 0.008 (2.667 ± 0.203)	0.050 (1.270)	0.015 (0.381)	0.015 (0.381)	0.015 (0.381)	
1206	0.126 ± 0.008 (3.200 ± 0.203)	0.063 (1.600)	0.015 (0.381)	0.020 + 0.005/- 0.010 (0.508 + 0.127/- 0.254)		

MECHANICAL SPECIFICATIONS	
Resistive Element	Passivated nichrome
Substrate Material	Alumina
Terminations	Pre-soldered or gold
Lead (Pb)-free Option	96.5 % Sn, 3.0 % Ag, 0.5 % Cu
Tin/Lead Option	Sn63
Lead (Pb)-free Finish and Tin / Lead	Hot solder dip

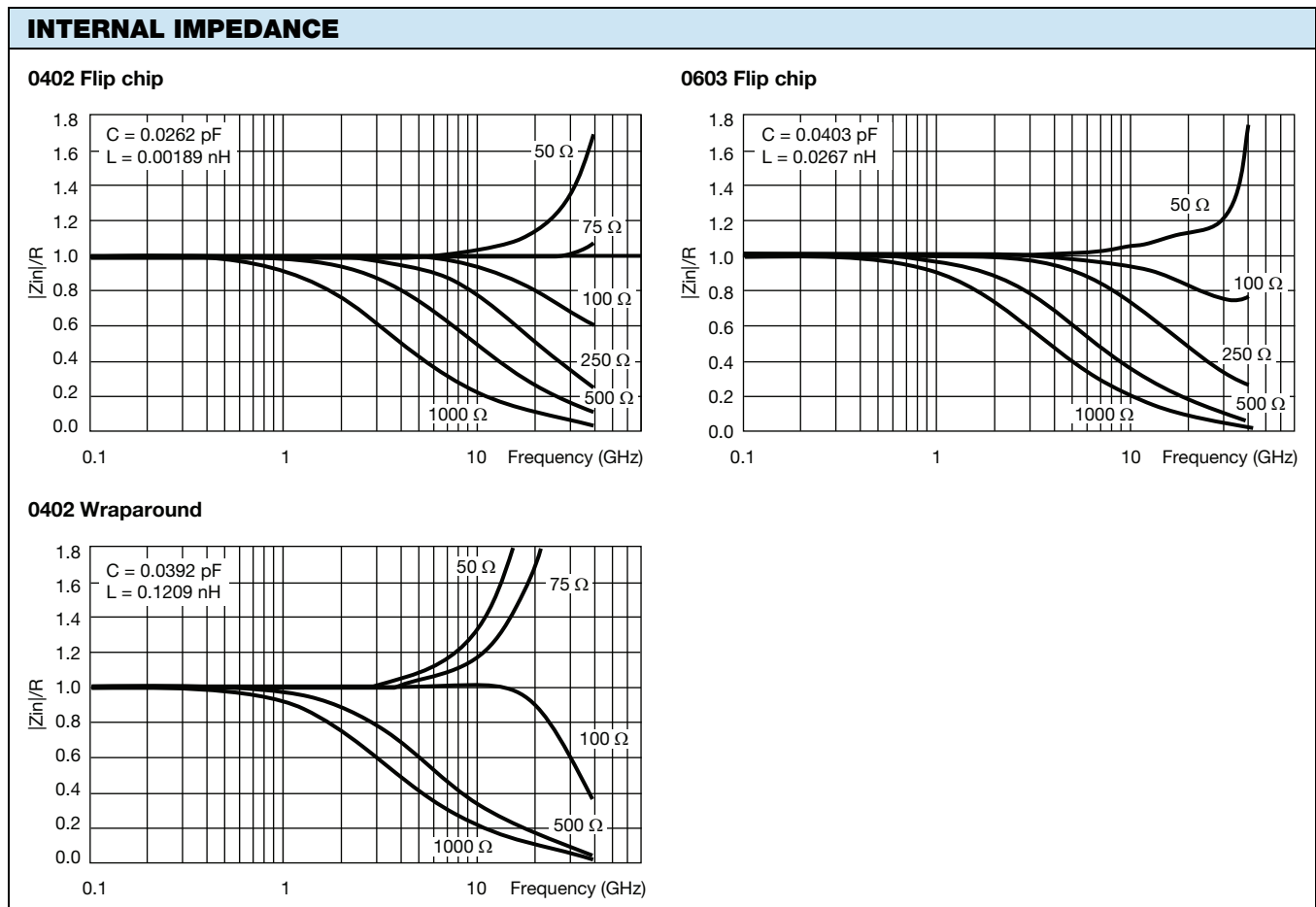
GLOBAL PART NUMBER INFORMATION																
New Global Part Numbering: FC1206E1001BBS																
F	C	1	2	0	6	E	1	0	0	1	B	B	T	S		
F	C	1	2	0	6	K	1	0	0	0	B	T	B	S	T	S
GLOBAL MODEL	CASE SIZE	TCR CHARACTERISTIC		RESISTANCE	TOLERANCE	TERMINATION (1, 2 or 3 digits)			PACKAGING							
FC	0402 0505 0603 0805 1005 1206	E = 25 ppm/°C H = 50 ppm/°C K = 100 ppm/°C	The first 3 digits are significant figures and the last digit specifies the number of zeros to follow. "R" designates the decimal point. Example: 10R0 = 10 Ω 1000 = 100 Ω 1001 = 1 kΩ		B = 0.1 % D = 0.5 % F = 1 % G = 2 % J = 5 %	T = top sided Au (gold) term Au over Ni epoxy bondable RoHS-compliant - e4 B = wraparound Sn/Pb solder 63 % Sn/37 % Pb with nickel barrier G = wraparound Au over Ni (gold) termination epoxy bondable RoHS-compliant - e4 TB = top sided Sn/Pb solder 63 % Sn/37 % Pb with nickel barrier TBS = top sided lead (Pb)-free solder with nickel barrier RoHS-compliant - e1 S = wraparound lead (Pb)-free solder 96.5 % Sn/3.0 % Ag/0.5 % Cu RoHS-compliant - e1			BS = BULK 100 min., 1 mult. WS = WAFFLE 100 min., 1 mult. TAPE AND REEL T0 = 100 min., 100 mult. T1 = 1000 min., 1000 mult. ⁽¹⁾ T3 = 300 min., 300 mult. T5 = 500 min., 500 mult. TF = full reel TS = 100 min., 1 mult.							
Historical Part Number Example: FC1206E1001BBT (for reference purposes only)																
FC	1206	E		1001	B	B	T									
SERIES	CASE SIZE	TCR CHARACTERISTIC		RESISTANCE	TOLERANCE	TERMINATION	PACKAGING									

Note
⁽¹⁾ Preferred packaging code

TYPICAL HIGH FREQUENCY PERFORMANCE ELECTRICAL MODEL AND TESTING

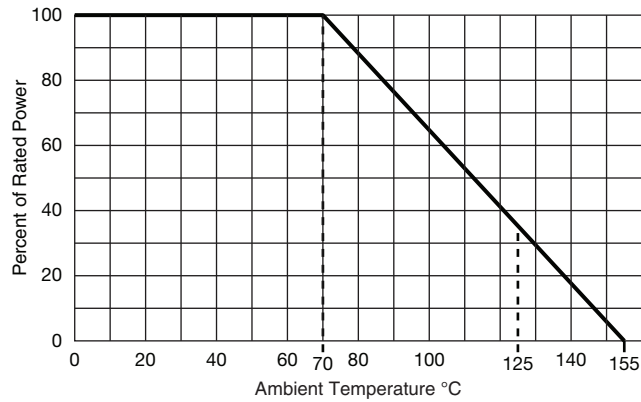


The lumped circuit above was used to model the data at the bonding pad-resistor reference plane. High frequency testing was performed by Modelithics, Inc. on parts mounted to quartz test boards. Quartz test boards were chosen to minimize the contribution of the board effects at high frequencies.

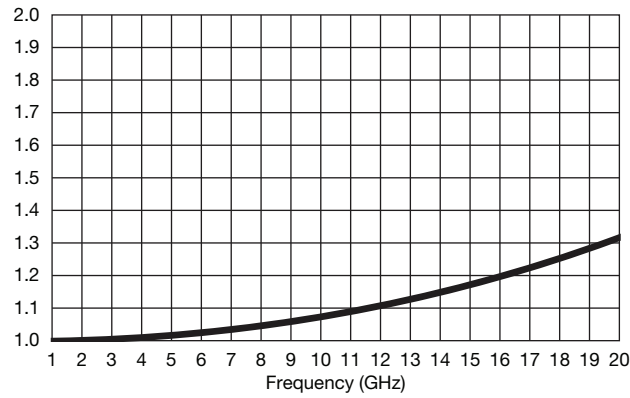




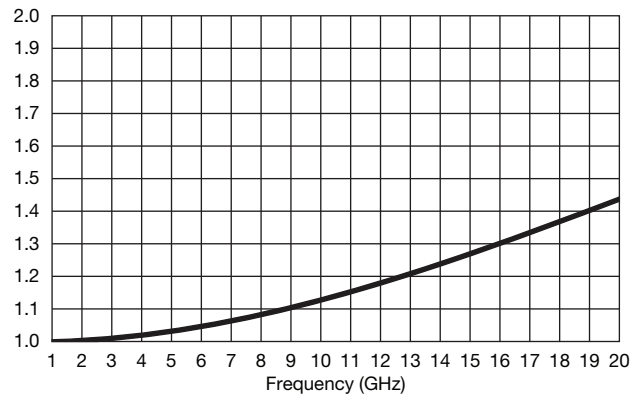
DERATING CURVE



VSWR FC Series 0402 size 50 Ω



VSWR FC Series 0402 size 100 Ω





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THE IDEAL CAPACITOR

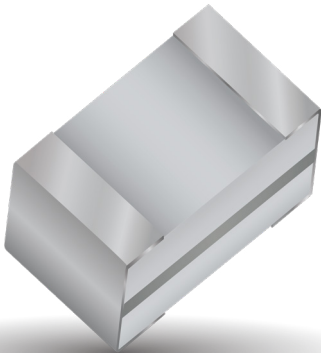
The non-ideal characteristics of a real capacitor can be ignored at low frequencies. Physical size imparts inductance to the capacitor and dielectric and metal electrodes result in resistive losses, but these often are of negligible effect on the circuit. At the very high frequencies of radio communication (>100MHz) and satellite systems (>1GHz), these effects become important. Recognizing that a real capacitor will exhibit inductive and resistive impedances in addition to capacitance, the ideal capacitor for these high frequencies is an ultra low loss component which can be fully characterized in all parameters with total repeatability from unit to unit.

Until recently, most high frequency/microwave capacitors were based on fired-ceramic (porcelain) technology. Layers of ceramic dielectric material and metal alloy electrode paste are interleaved and then sintered in a high temperature oven. This technology exhibits component variability in dielectric quality (losses, dielectric constant and insulation resistance), variability in electrode conductivity and variability in physical size (affecting inductance). An alternate thin-film technology has been developed which virtually eliminates these variances. It is this technology which has been fully incorporated into Accu-P® and Accu-P® to provide high frequency capacitors exhibiting truly ideal characteristics.

The main features of Accu-P® may be summarized as follows:

- High purity of electrodes for very low and repeatable ESR.
- Highly pure, low-K dielectric for high breakdown field, high insulation resistance and low losses to frequencies above 40GHz.
- Very tight dimensional control for uniform inductance, unit to unit.
- Very tight capacitance tolerances for high frequency signal applications.

This accuracy sets apart these Thin-Film capacitors from ceramic capacitors so that the term Accu has been employed as the designation for this series of devices, an abbreviation for “accurate.”

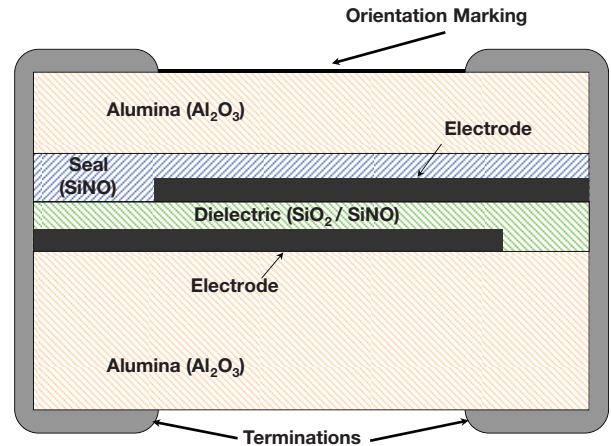


THIN-FILM TECHNOLOGY

Thin-film technology is commonly used in producing semiconductor devices. In the last two decades, this technology has developed tremendously, both in performance and in process control. Today's techniques enable line definitions of below 1µm, and the controlling of thickness of layers at 100Å (10-2µm). Applying this technology to the manufacture of capacitors has enabled the development of components where both electrical and physical properties can be tightly controlled.

The thin-film production facilities at KYOCERA AVX consist of:

- Class 1000 clean rooms, with working areas under laminar-flow hoods of class 100, (below 100 particles per cubic foot larger than 0.5µm).
- High vacuum metal deposition systems for high-purity electrode construction.
- Photolithography equipment for line definition down to 2.0µm accuracy.
- Plasma-enhanced CVD for various dielectric depositions (CVD=Chemical Vapor Deposition).
- High accuracy, microprocessor-controlled dicing saws for chip separation.
- High speed, high accuracy sorting to ensure strict tolerance adherence.



ACCU-P® CAPACITOR STRUCTURE

ACCU-P® TECHNOLOGY

The use of very low-loss dielectric materials, silicon dioxide and silicon oxynitride, in conjunction with highly conductive electrode metals results in low ESR and high Q. These high-frequency characteristics change at a slower rate with increasing frequency than for ceramic microwave capacitors.

Because of the thin-film technology, the above-mentioned frequency characteristics are obtained without significant compromise of properties required for surface mounting.

The main Accu-P® properties are:

- Internationally agreed sizes with excellent dimensional control.
- Ultra small size chip capacitors (1005) are available.
- Ultra tight capacitance tolerances.
- Low ESR at VHF, UHF and microwave frequencies.
- Enhanced RF power handling capability.
- High stability with respect to time, temperature, frequency and voltage variation.
- Nickel/solder-coated terminations to provide excellent solderability and leach resistance.

ACCU-P® FEATURES

Accu-P® meets the fast-growing demand for low-loss (high-Q) capacitors for use in surface mount technology especially for the mobile communications market, such as cellular radio of 450 and 900 MHz, UHF walkie-talkies, UHF cordless telephones to 2.3 GHz, low noise blocks at 11-12.5 GHz and for other VHF, UHF and microwave applications.

Accu-P® is currently unique in its ability to offer very low capacitance values (0.05pF) and very tight capacitance tolerances (± 0.01 pF).

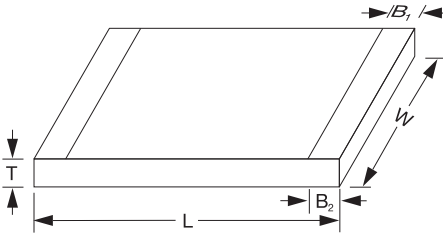
- The RF power handling capability of the Accu-P® allows for its usage in both small signal and RF power applications.
- Thin Film Technology guarantees minimal batch to batch variability of parameters at high frequency.
- Inspection test and quality control procedures in accordance with ISO 9001, CECC, IECQ and USA MIL Standards yield products of the highest quality.
- Hand soldering Accu-P®: Due to their construction utilizing relatively high thermal conductivity materials, Accu-P's have become the preferred device in R & D labs and production environments where hand soldering is used.

APPLICATIONS

- Cellular Communications
- CT2/PCN (Cordless Telephone/Personal Comm. Networks)
- Satellite TV
- Cable TV
- GPS (Global Positioning Systems)
- Vehicle Location Systems
- Vehicle Alarm Systems
- Paging
- Military Communications
- Radar Systems
- Video Switching
- Test & Measurements
- Filters
- VCO's
- Matching Networks
- RF Amplifiers

APPROVALS

- ISO 9001
- IATF 16949:2016



ACCU-P® (SIGNAL AND POWER TYPE CAPACITORS)

	01005*	0201*	0402*	0603*	0805*	1210
L	0.405±0.020 (0.016±0.001)	0.60±0.05 (0.023±0.002)	1.00±0.1 (0.039±0.004)	1.60±0.1 (0.063±0.004)	2.01±0.1 (0.079±0.004)	3.02±0.1 (0.119±0.004)
W	0.215±0.020 (0.0085±0.002)	0.325±0.050 (0.0128±0.002)	0.55±0.07 (0.022±0.003)	0.81±0.1 (0.032±0.004)	1.27±0.1 (0.050±0.004)	2.5±0.1 (0.100±0.004)
T	0.145±0.020 (0.006±0.001)	0.225±0.050 (0.009±0.002)	0.40±0.1 (0.016±0.004)	0.63±0.1 (0.025±0.004)	0.93±0.2 (0.036±0.008)	0.93±0.2 (0.036±0.008)
B1	0.00 ^{+0.1} _{-0.0} (0.004 ^{+0.004} _{-0.000})	0.10±0.10 (0.004±0.004)	(0.0 ^{+0.1} _{-0.0}) (0.00 ^{+0.004} _{-0.000})	0.35±0.15 (0.014±0.006)	0.30±0.1 (0.012±0.004)	0.43±0.1 (0.017±0.004)
B2	0.15±0.05 (0.000±0.002)	0.15±0.05 (0.006±0.002)	0.20±0.1 (0.008±0.004)	0.35±0.15 (0.014±0.006)	0.30±0.1 (0.012±0.004)	0.43±0.1 (0.017±0.004)

*Mount Black Side Up

DIMENSIONS: millimeters (inches)

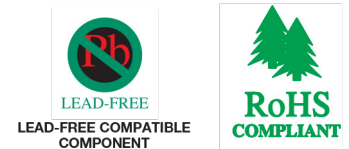
HOW TO ORDER

0402	3	J	4R7	A	B	S	TR	\500
Size C005 0201 0402 0603 0805 1210*	Voltage 2 = 200V 1 = 100V 5 = 50V 3 = 25V Y = 16V Z = 10V	Temperature Coefficient (1) J = 0±30ppm/°C (-55°C to +125°C) K = 0±60ppm/°C (-55°C to +125°)	Capacitance Capacitance expressed in pF. (2 significant digits + number of zeros) for values <10pF, letter R denotes decimal point. Example: 68pF = 680 8.2pF = 8R2	Tolerance for C<2.0pF* Z = ±0.01pF P = ±0.02pF Q = ±0.03pF A = ±0.05pF B = ±0.1pF C = ±0.25pF for C<3.0pF Q = ±0.03pF A = ±0.05pF B = ±0.1pF C = ±0.25pF for C<5.6pF A = ±0.05pF B = ±0.1pF C = ±0.25pF for 5.6pF<C<10pF B = ±0.1pF C = ±0.25pF D = ±0.5pF for C<10pF F = ±1% G = ±2% J = ±5%	Specification Code B = Accu-P® technology	Termination Code W=Nickel/Solder Coated Accu-P® 0402 Sn90, Pb10*** T=Nickel/High Temperature Solder Coated Accu-P® 0805**, 1210** Sn96, Ag4 Nickel/Solder Coated Accu-P® 0603*** Sn63, Pb37 **S=Nickel/Lead Free Solder Coated Accu-P® 1005, 0201 0402, 0603 Sn100	Packaging Code TR = Tape & Reel	Option

**RoHS compliant
*** Not RoHS Compliant

The following 3 digit capacitance codes should be used for ordering Accu-P® capacitors	
CAPACITANCE CODE	EXAMPLE
0.00 to 0.99pF Rxx	0.15pF = 04023JR15ABSTR
1.00 to 1.99pF Axx	1.55pF = 04023JA55PBSTR
2.00 to 2.99pF Bxx	2.85pF = ...C85...
3.00 to 3.99pF Cxx	3.85pF = ...C85...
4.00 to 4.99pF Dxx	4.85pF = ...D85...
5.00 to 5.99pF Exx	5.85pF = ...E85...
6.00 to 6.99pF Fxx	6.85pF = ...F85...
7.00 to 7.99pF Gxx	7.85pF = ...G85...
8.00 to 8.99pF Hxx	8.85pF = ...H85...
9.00 to 9.99pF Jxx	9.85pF = ...J85...
10.0 to 19.9pF Kxx	13.8pF = ...K38...
20.0 to 29.9pF Lxx	22.5pF = ...L25...
30.0 to 39.9pF Mxx	33.8pF = ...M38...
40.0 to 49.9pF Nxx	43.5pF = ...N35...

(1) TC's shown are per EIA/IEC Specifications.
* Tolerances as tight as ±0.01pF are available. Please consult the factory.



For RoHS compliant products, please select correct termination style.

ELECTRICAL SPECIFICATIONS

Operating and Storage Temperature Range	-55°C to +125°C
Temperature Coefficients (1)	0 ± 30ppm/°C dielectric code "J" / 0 ± 60ppm/°C dielectric code "K"
Capacitance Measurement	1 MHz, 1 Vrms
Insulation Resistance (IR)	≥1011 Ohms (≥10 ¹⁰ Ohms for 0201 and 0402 size)
Proof Voltage	2.5 U _R for 5 secs.
Aging Characteristic	Zero
Dielectric Absorption	0.01%

Thin-Film RF/Microwave Capacitor Technology

Accu-P® Series

Single and Power Type Capacitors



TEMP. COEFFICIENT CODE

"J" = 0±30PPM/°C (-55°C TO +125°C)⁽²⁾ "K" = 0±60PPM/°C (-55°C TO +125°C)⁽²⁾

Size			C005						0201						0402						0603						0805						1210	
Size Code	Cap Code	Cap Code	16	100	50	25	16	10	200	100	50	25	16	10	200	100	50	25	100	50	25	100	50											
Cap in pF	Cap code	Cap code																																
0.1	—	0R1																																
0.2	—	0R2																																
0.3	—	0R3																																
0.4	—	0R4																																
0.5	—	0R5																																
0.6	—	0R6																																
0.7	—	0R7																																
0.8	—	0R8																																
0.9	—	0R9																																
1.0	—	1R0																																
1.1	—	1R1																																
1.2	—	1R2																																
1.3	—	1R3																																
1.4	—	1R4																																
1.5	—	1R5																																
1.6	—	1R6																																
1.7	—	1R7																																
1.8	—	1R8																																
1.9	—	1R9																																
2.0	—	2R0																																
2.1	—	2R1																																
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7.5	—	7R5																																
8.2	—	8R2																																
9.1	—	9R1																																
10.0	—	100																																
11.0	—	110																																
12.0	—	120																																
13.0	—	130																																
14.0	—	140																																
15.0	—	150																																
16.0	—	160																																
17.0	—	170																																
18.0	—	180																																
19.0	—	190																																
20.0	—	200																																
21.0	—	210																																
22.0	—	220																																
24.0	—	240																																
27.0	—	270																																
30.0	—	300																																
33.0	—	330																																
39.0	—	390																																
47.0	—	470																																
56.0	—	560																																
68.0	—	680																																

(1) For capacitance values higher than listed in table, please consult factory.
 (2) TC shown is per EIA/IEC Specifications.
 These values are produced with "K" temperature coefficient code only.

Intermediate values are available within the indicated range.



The Important Information/Disclaimer is incorporated in the catalog where these specifications came from or available online at www.avx.com/disclaimer/ by reference and should be reviewed in full before placing any order.

Capacitance @ 1MHz and Tolerance		Self Resonance Frequency (GHz) Typ.	Q Standard Value @ 1GHz		Frequency 900MHz			Frequency 1900MHz			Frequency 2400MHz		
C (pF)	Tol.		Typ.	Min.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.
0.05	±0.02	20.9	599	402	0.055	650	3220	0.056	265	4010	0.057	195	4450
0.1	±0.02	19.4	574	316	0.110	614	2682	0.112	246	3036	0.113	188	3113
0.15	±0.02	17.9	510	280	0.163	550	2087	0.166	220	2404	0.168	170	2441
0.2	±0.02	16.4	445	245	0.216	520	1693	0.220	210	1971	0.223	160	1970
0.25	±0.02	15.5	436	240	0.262	510	1371	0.268	204	1604	0.272	153	1646
0.3	±0.02	14.6	427	235	0.309	500	1149	0.316	199	1337	0.320	146	1421
0.35	±0.02	14.1	423	232	0.360	494	1001	0.369	196	1177	0.374	144	1265
0.4	±0.02	12.5	418	230	0.411	489	874	0.421	193	1038	0.427	142	1129
0.45	±0.02	11.9	413	227	0.461	484	819	0.473	191	972	0.481	140	1066
0.5	±0.02	11.3	408	224	0.512	478	765	0.526	188	906	0.535	138	1003
0.55	±0.02	10.9	403	222	0.563	473	710	0.578	186	840	0.588	137	940
0.6	±0.02	10.4	398	219	0.614	468	667	0.631	183	791	0.642	135	882
0.65	±0.02	10.0	394	217	0.664	462	624	0.683	181	742	0.695	133	825
0.7	±0.02	9.5	389	214	0.715	457	580	0.735	178	693	0.749	131	767
0.75	±0.02	9.3	384	211	0.766	452	557	0.788	176	664	0.802	129	729
0.8	±0.02	9.1	379	209	0.817	446	534	0.840	173	635	0.856	127	692
0.85	±0.02	8.9	374	206	0.868	441	511	0.893	171	606	0.909	126	654
0.9	±0.02	8.8	370	203	0.918	436	487	0.945	168	577	0.963	124	616
0.95	±0.02	8.6	365	201	0.969	430	464	0.998	166	548	1.016	122	579
1	±0.02	8.4	360	198	1.020	425	441	1.050	163	519	1.070	120	541
1.05	±0.02	8.2	358	197	1.078	421	426	1.112	161	502	1.134	119	523
1.1	±0.02	8.0	355	195	1.135	418	410	1.173	159	486	1.199	117	505
1.15	±0.02	7.8	353	194	1.193	414	395	1.235	157	469	1.263	116	488
1.2	±0.02	7.6	350	193	1.251	411	379	1.296	155	452	1.327	115	470
1.25	±0.02	7.5	348	191	1.308	407	364	1.358	153	436	1.392	114	452
1.3	±0.02	7.4	345	190	1.366	403	348	1.419	151	419	1.456	112	434
1.35	±0.02	7.3	343	189	1.424	400	333	1.481	149	402	1.520	111	416
1.4	±0.02	7.2	340	187	1.481	396	317	1.542	147	386	1.585	110	398
1.45	±0.02	7.1	338	186	1.539	393	302	1.604	145	369	1.649	109	381
1.5	±0.02	7.0	335	184	1.597	389	287	1.665	144	353	1.713	107	363
1.55	±0.02	6.8	332	183	1.642	386	282	1.714	142	347	1.764	106	358
1.6	±0.02	6.7	330	181	1.687	382	277	1.762	141	342	1.815	105	352
1.65	±0.02	6.6	327	180	1.732	378	272	1.810	140	337	1.866	104	347
1.7	±0.02	6.5	324	178	1.777	375	267	1.859	138	331	1.917	103	342
1.75	±0.02	6.4	321	176	1.822	371	262	1.907	137	326	1.968	102	337
1.8	±0.02	6.3	318	175	1.866	367	257	1.955	136	321	2.018	101	331
1.85	±0.02	6.2	315	173	1.911	364	252	2.003	134	316	2.069	100	326
1.9	±0.02	6.2	312	172	1.956	360	247	2.052	133	310	2.120	99	321
1.95	±0.02	6.1	309	170	2.001	357	242	2.100	132	305	2.171	98	316
2	±0.03	6.0	306	168	2.046	353	237	2.148	131	300	2.222	97	310
2.1	±0.03	5.9	301	166	2.150	348	232	2.263	128	293	2.344	95	303
2.2	±0.03	5.7	296	163	2.254	343	227	2.377	125	287	2.467	93	296
2.3	±0.03	5.6	292	160	2.358	337	222	2.491	122	281	2.590	91	289
2.4	±0.03	5.5	287	158	2.462	332	217	2.606	120	274	2.712	89	282
2.5	±0.03	5.4	282	155	2.566	327	212	2.720	117	268	2.835	87	275
2.6	±0.03	5.3	277	152	2.670	322	207	2.834	114	262	2.958	85	268
2.7	±0.03	5.2	272	150	2.773	317	202	2.949	112	255	3.080	83	261
2.8	±0.03	5.1	269	148	2.878	312	199	3.066	110	252	3.209	81	258
2.9	±0.03	5.0	265	146	2.983	308	196	3.184	108	248	3.337	80	254
3	±0.03	4.9	261	144	3.088	304	193	3.301	106	245	3.465	78	251
3.1	±0.05	4.8	257	141	3.192	299	190	3.419	105	241	3.593	77	247
3.2	±0.05	4.7	253	139	3.297	295	187	3.536	103	238	3.722	76	244
3.3	±0.05	4.6	250	137	3.402	291	185	3.654	101	234	3.850	74	240
3.4	±0.05	4.6	246	135	3.506	286	182	3.771	99	231	3.978	73	237
3.5	±0.05	4.5	242	133	3.611	282	179	3.889	98	227	4.107	71	233
3.6	±0.05	4.5	238	131	3.716	278	176	4.006	96	224	4.235	70	230
3.7	±0.05	4.4	234	129	3.820	273	173	4.124	94	220	4.363	69	226
3.8	±0.05	4.4	230	127	3.925	269	170	4.241	92	217	4.492	67	223

0201 Typical Electrical Tables

Capacitance @ 1MHz and Tolerance		Self Resonance Frequency (GHz) Typ.	Q Standard Value @ 1GHz		Frequency 900MHz			Frequency 1900MHz			Frequency 2400MHz		
C (pF)	Tol.		Typ.	Min.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.
3.9	±0.05	4.3	227	125	4.030	265	167	4.359	91	213	4.620	66	219
4	±0.05	4.3	224	123	4.138	262	165	4.484	89	210	4.760	65	216
4.1	±0.05	4.2	222	122	4.247	259	162	4.610	88	207	4.901	64	213
4.2	±0.05	4.2	220	121	4.356	257	159	4.735	87	204	5.041	63	210
4.3	±0.05	4.1	218	120	4.464	254	157	4.860	86	201	5.181	62	207
4.4	±0.05	4.1	216	119	4.573	252	154	4.986	85	198	5.322	61	204
4.5	±0.05	4.0	214	118	4.682	249	152	5.111	83	195	5.462	60	201
4.6	±0.05	4.0	212	116	4.790	246	149	5.237	82	192	5.602	59	198
4.7	±0.05	3.9	209	115	4.899	244	147	5.362	81	189	5.743	58	195
5.1	±0.05	3.8	201	110	5.334	233	136	5.863	76	178	6.304	54	183
5.6	±0.05	3.6	190	105	5.877	220	124	6.490	70	163	7.006	49	168
6.2	±0.1	3.5	177	97	6.488	208	126	7.290	65	167	7.993	45	174
6.8	±0.1	3.3	164	90	7.100	195	128	8.090	60	171	8.980	41	179
7.5	±0.1	3.2	153	84	7.901	182	125	9.129	56	166	10.27	38	173
8.2	±0.1	3.0	142	78	8.701	168	121	10.17	52	160	11.56	34	167
9.1	±0.1	2.9	135	74	9.676	159	118	11.57	49	154	13.49	32	161
10	±1%	2.8	128	70	10.65	151	114	12.96	45	148	15.41	29	155
11	±1%	2.7	120	66	11.73	141	110	14.52	42	142	17.55	27	148
12	±1%	2.5	112	62	12.82	132	105	16.07	39	135	19.68	24	141
13	±1%	2.4	105	58	13.92	124	104	17.82	36	135	22.38	22	142
14	±1%	2.4	98	54	15.02	116	103	19.57	32	135	25.08	19	142
15	±1%	2.3	91	50	16.12	108	102	21.32	29	135	27.78	17	143
16	±1%	2.2	86	47	17.37	102	103	24.04	27	135	NA	NA	NA
17	±1%	2.2	81	44	18.63	96	105	26.76	25	136	NA	NA	NA
18	±1%	2.1	76	42	19.88	90	106	29.48	23	136	NA	NA	NA
19	±1%	2.1	71	39	21.14	83	108	32.20	21	136	NA	NA	NA
20	±1%	2.1	65	36	22.39	77	109	34.92	19	136	NA	NA	NA
22	±1%	2.0	55	30	24.90	65	112	40.36	15	137	NA	NA	NA

Capacitance @ 1MHz and Tolerance		Self Resonance Frequency (GHz) Typ.	Q Standard Value @ 1GHz		Frequency 900MHz			Frequency 1900MHz			Frequency 2400MHz		
C (pF)	Tol.		Typ.	Min.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.
0.05	±0.02	20.9	856	471	0.06	881	1411	0.06	562	1216	0.06	498	983
0.1	±0.02	19.4	848	466	0.11	873	1316	0.11	554	1115	0.11	490	914
0.15	±0.02	17.9	840	462	0.16	866	1222	0.16	547	1013	0.16	482	845
0.2	±0.02	16.4	832	457	0.21	858	1128	0.21	539	912	0.22	474	776
0.25	±0.02	15.5	823	453	0.26	850	1033	0.27	532	810	0.27	465	707
0.3	±0.02	14.6	815	448	0.31	842	939	0.32	525	708	0.32	457	638
0.35	±0.02	14.1	807	444	0.36	834	844	0.37	517	607	0.37	449	569
0.4	±0.02	12.5	799	439	0.41	827	750	0.42	510	505	0.42	441	500
0.45	±0.02	11.9	791	435	0.46	819	667	0.47	502	458	0.48	432	453
0.5	±0.02	11.3	783	430	0.51	811	583	0.52	495	410	0.53	424	407
0.55	±0.02	10.9	774	426	0.57	803	500	0.57	487	363	0.58	416	360
0.6	±0.02	10.4	766	421	0.62	796	465	0.62	480	343	0.63	408	339
0.65	±0.02	10.0	758	417	0.67	788	431	0.67	472	322	0.68	399	317
0.7	±0.02	9.5	750	413	0.72	780	396	0.72	465	302	0.73	391	296
0.75	±0.02	9.3	746	410	0.77	776	375	0.78	456	290	0.79	381	285
0.8	±0.02	9.1	743	408	0.82	772	354	0.83	447	277	0.84	370	273
0.85	±0.02	9.0	739	406	0.87	768	334	0.88	438	265	0.89	360	262
0.9	±0.02	8.8	735	404	0.92	764	313	0.93	429	253	0.95	350	250
0.95	±0.02	8.4	732	402	0.97	760	292	0.98	420	240	1.00	339	239
1	±0.02	8.0	728	400	1.02	756	271	1.04	411	228	1.05	329	227
1.05	±0.02	7.9	725	398	1.07	752	258	1.09	406	221	1.11	323	221
1.1	±0.02	7.8	721	397	1.12	749	245	1.14	401	214	1.16	318	214
1.15	±0.02	7.6	718	395	1.17	745	232	1.20	396	207	1.22	312	208
1.2	±0.02	7.4	714	393	1.22	742	218	1.25	391	200	1.27	306	202
1.25	±0.02	7.2	711	391	1.27	738	205	1.31	386	193	1.32	301	195
1.3	±0.02	7.0	707	389	1.32	734	192	1.36	381	185	1.38	295	189
1.35	±0.02	6.9	704	387	1.37	731	179	1.41	376	178	1.43	289	183
1.4	±0.02	6.8	700	385	1.42	727	165	1.47	371	171	1.49	283	177
1.45	±0.02	6.7	697	383	1.47	724	152	1.52	366	164	1.54	278	170
1.5	±0.02	6.5	693	381	1.52	720	139	1.58	361	157	1.60	272	164
1.55	±0.02	6.5	690	379	1.56	716	135	1.62	358	153	1.65	269	159
1.6	±0.02	6.5	686	377	1.61	713	130	1.67	355	148	1.70	267	155
1.65	±0.02	6.5	683	375	1.66	709	126	1.72	352	143	1.76	264	150
1.7	±0.02	6.4	679	373	1.71	705	122	1.77	349	139	1.81	261	146
1.75	±0.02	6.3	676	372	1.75	702	118	1.82	347	134	1.86	259	141
1.8	±0.02	6.2	672	370	1.80	698	113	1.87	344	130	1.92	256	137
1.85	±0.02	6.1	669	368	1.85	694	109	1.92	341	125	1.97	253	132
1.9	±0.02	6.0	665	366	1.90	690	105	1.97	338	121	2.02	251	128
1.95	±0.02	5.9	662	364	1.94	687	101	2.01	335	116	2.08	248	123
2	±0.03	5.7	658	362	1.99	683	96	2.06	332	112	2.13	245	119
2.1	±0.03	5.4	651	358	2.10	676	93	2.18	326	108	2.26	241	115
2.2	±0.03	5.1	643	354	2.21	669	89	2.30	321	104	2.38	236	112
2.3	±0.03	5.0	636	350	2.31	662	85	2.42	315	101	2.51	231	109
2.4	±0.03	4.9	629	346	2.42	656	81	2.54	309	97	2.64	226	106
2.5	±0.03	4.7	622	342	2.53	649	77	2.65	303	94	2.76	221	102
2.6	±0.03	4.6	614	338	2.64	642	74	2.77	298	90	2.89	216	99
2.7	±0.03	4.5	607	334	2.75	635	70	2.89	292	86	3.02	211	96
2.8	±0.03	4.5	600	330	2.85	628	68	3.01	288	83	3.15	207	92
2.9	±0.03	4.4	592	326	2.95	621	66	3.13	283	80	3.28	203	88
3	±0.03	4.4	585	322	3.06	614	64	3.24	279	76	3.41	200	84
3.1	±0.05	4.4	578	318	3.16	607	62	3.36	274	73	3.54	196	80
3.2	±0.05	4.3	570	314	3.27	600	60	3.48	270	70	3.67	192	76
3.3	±0.05	4.3	563	310	3.37	593	58	3.60	265	67	3.80	188	72
3.4	±0.05	4.3	556	306	3.47	586	57	3.71	261	63	3.93	184	68
3.5	±0.05	4.2	548	302	3.58	579	55	3.83	256	60	4.06	180	64
3.6	±0.05	4.2	541	298	3.68	572	53	3.95	252	57	4.19	177	60
3.7	±0.05	4.1	534	294	3.78	565	51	4.06	247	54	4.32	173	56
3.8	±0.05	4.0	526	289	3.89	558	49	4.18	243	50	4.45	169	52

Capacitance @ 1MHz and Tolerance		Self Resonance Frequency (GHz) Typ.	Q Standard Value @ 1GHz		Frequency 900MHz			Frequency 1900MHz			Frequency 2400MHz		
C (pF)	Tol.		Typ.	Min.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.
3.9	±0.05	3.9	519	285	3.99	551	47	4.30	238	47	4.58	165	48
4	±0.05	3.9	513	282	4.10	545	47	4.42	235	47	4.73	162	48
4.1	±0.05	3.8	507	279	4.20	539	47	4.55	232	46	4.87	160	48
4.2	±0.05	3.8	501	275	4.30	534	46	4.67	228	46	5.01	157	48
4.3	±0.05	3.7	495	272	4.41	528	46	4.79	225	46	5.16	154	48
4.4	±0.05	3.7	489	269	4.51	522	46	4.92	222	46	5.30	151	47
4.5	±0.05	3.6	483	265	4.61	516	46	5.04	219	45	5.44	149	47
4.6	±0.05	3.6	477	262	4.72	511	45	5.16	216	45	5.59	146	47
4.7	±0.05	3.5	471	259	4.82	505	45	5.29	213	45	5.73	143	47
5.1	±0.05	3.4	446	245	5.23	482	44	5.78	200	43	6.30	133	47
5.6	±0.05	3.3	416	229	5.75	453	43	6.40	184	42	7.02	119	46
6.2	±0.1	3.0	388	213	6.41	427	44	7.26	167	44	8.11	107	47
6.8	±0.1	2.8	360	198	7.07	400	44	8.12	150	45	9.19	95	48
7.5	±0.1	2.7	338	186	7.85	378	45	9.17	139	47	10.57	86	49
8.2	±0.1	2.6	315	173	8.62	356	45	10.22	128	48	11.95	77	50
9.1	±0.1	2.5	292	160	9.63	333	45	11.75	115	47	14.23	69	50
10	±1%	2.4	268	148	10.65	310	45	13.28	103	47	16.50	61	49
11	±1%	2.3	242	133	11.77	285	44	14.98	89	46	19.04	51	49
12	±1%	2.2	217	119	12.90	259	44	16.68	75	45	21.57	42	48
13	±1%	2.2	202	111	14.03	241	44	18.83	68	47	25.73	38	49
14	±1%	2.1	187	103	15.17	223	44	20.97	62	49	29.89	33	49
15	±1%	2.1	172	94	16.30	204	45	23.12	56	51	34.05	29	50
16	±1%	2.0	157	87	17.53	187	44	25.91	50	49	41.44	25	49
17	±1%	1.9	143	79	18.75	169	43	28.70	45	46	48.82	21	47
18	±1%	1.8	129	71	19.98	152	42	31.49	39	44	56.21	17	46
19	±1%	1.8	121	67	21.11	143	42	33.51	36	44	60.92	15	47
20	±1%	1.8	110	61	22.25	131	41	35.53	33	43	65.63	14	48
22	±1%	1.8	98	54	24.51	116	41	39.57	26	42	75.05	10	51
24	±1%	1.8	87	48	27.51	104	37	54.94	21	35	NA	NA	NA
27	±1%	1.7	70	39	32.01	85	32	77.98	13	23	NA	NA	NA
30	±1%	1.7	65	36	35.89	78	28	106.50	10	12	NA	NA	NA
33	±1%	1.7	60	33	40.05	74	27	NA	NA	NA	NA	NA	NA
36	±1%	1.7	58	32	45.13	71	28	NA	NA	NA	NA	NA	NA
39	±1%	1.7	56	31	50.21	69	28	NA	NA	NA	NA	NA	NA
43	±1%	1.6	53	29	56.98	66	29	NA	NA	NA	NA	NA	NA
47	±1%	1.6	50	28	63.75	63	30	NA	NA	NA	NA	NA	NA
51	±1%	1.6	48	26	70.53	60	31	NA	NA	NA	NA	NA	NA
56	±1%	1.6	44	24	78.99	56	33	NA	NA	NA	NA	NA	NA
58	±1%	1.6	42	23	83.54	54	34	NA	NA	NA	NA	NA	NA
68	±1%	1.6	32	18	106.28	42	40	NA	NA	NA	NA	NA	NA

Capacitance @ 1MHz and Tolerance		Self Resonance Frequency (GHz) Typ.	Q Standard Value @ 1GHz		Frequency 900MHz			Frequency 1900MHz			Frequency 2400MHz		
C (pF)	Tol.		Typ.	Min.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.
0.05	±0.02	25.6	1200	660	0.06	1333	945	0.06	556	832	0.06	397	880
0.1	±0.02	18.1	1156	636	0.11	1284	675	0.11	535	628	0.11	382	667
0.15	±0.02	14.8	1111	611	0.16	1235	555	0.16	514	533	0.16	367	567
0.2	±0.02	12.8	1067	587	0.21	1185	483	0.21	494	474	0.22	353	505
0.25	±0.02	11.4	1022	562	0.26	1136	433	0.27	473	433	0.27	338	462
0.3	±0.02	10.4	978	538	0.31	1086	397	0.32	453	402	0.32	323	430
0.35	±0.02	9.7	933	513	0.36	1037	368	0.37	432	378	0.37	309	404
0.4	±0.02	9.0	889	489	0.41	988	345	0.42	412	358	0.42	294	383
0.45	±0.02	8.5	844	464	0.46	938	326	0.47	391	341	0.48	279	365
0.5	±0.02	8.1	800	440	0.51	889	310	0.52	370	327	0.53	265	350
0.55	±0.02	7.7	788	434	0.57	875	296	0.57	363	315	0.58	261	337
0.6	±0.02	7.4	777	427	0.62	860	283	0.62	356	304	0.63	258	326
0.65	±0.02	7.1	765	421	0.67	846	273	0.67	348	294	0.68	255	315
0.7	±0.02	6.8	754	414	0.72	832	263	0.72	341	285	0.73	252	306
0.75	±0.02	6.6	742	408	0.77	817	254	0.78	334	277	0.79	248	298
0.8	±0.02	6.4	730	402	0.82	803	247	0.83	326	270	0.84	245	290
0.85	±0.02	6.2	719	395	0.87	789	239	0.88	319	264	0.89	242	283
0.9	±0.02	6.0	707	389	0.92	775	233	0.93	312	258	0.95	239	277
0.95	±0.02	5.9	696	383	0.97	760	227	0.98	304	252	1.00	235	271
1	±0.02	5.7	684	376	1.019	746	216	1.061	297	242	1.101	232	260
1.05	±0.02	5.6	667	367	1.076	731	213	1.126	290	239	1.171	226	256
1.1	±0.02	5.4	649	357	1.134	717	210	1.190	282	236	1.241	220	253
1.15	±0.02	5.3	632	347	1.192	702	206	1.254	275	233	1.311	214	250
1.2	±0.02	5.2	614	338	1.250	687	203	1.318	267	230	1.381	209	247
1.25	±0.02	5.1	605	333	1.307	677	200	1.382	262	227	1.451	203	244
1.3	±0.02	5.0	596	328	1.365	667	197	1.446	257	224	1.521	197	241
1.35	±0.02	4.9	587	323	1.423	658	194	1.511	252	221	1.591	191	238
1.4	±0.02	4.8	578	318	1.481	648	190	1.575	247	218	1.661	185	235
1.45	±0.02	4.8	569	313	1.538	638	187	1.639	242	215	1.731	179	232
1.5	±0.02	4.7	560	308	1.596	628	184	1.703	237	212	1.801	173	229
1.55	±0.02	4.6	551	303	1.645	620	181	1.760	233	209	1.866	170	226
1.6	±0.02	4.5	542	298	1.694	611	178	1.817	228	206	1.930	166	222
1.65	±0.02	4.5	534	293	1.743	603	175	1.874	224	203	1.995	163	219
1.7	±0.02	4.4	525	289	1.792	595	172	1.931	219	200	2.060	159	216
1.75	±0.02	4.3	516	284	1.841	587	169	1.988	215	197	2.124	156	213
1.8	±0.02	4.2	507	279	1.890	578	166	2.045	211	194	2.189	153	209
1.85	±0.02	4.2	498	274	1.939	570	163	2.102	206	191	2.253	149	206
1.9	±0.02	4.1	490	269	1.988	562	160	2.158	202	188	2.318	146	203
1.95	±0.02	4.1	481	264	2.037	553	157	2.215	197	185	2.383	142	199
2	±0.03	4.0	472	260	2.086	545	154	2.272	193	182	2.447	139	196
2.1	±0.03	3.9	462	254	2.190	535	151	2.402	187	180	2.604	134	193
2.2	±0.03	3.8	452	249	2.295	524	148	2.532	181	177	2.761	129	191
2.3	±0.03	3.8	442	243	2.400	514	145	2.662	175	175	2.917	124	188
2.4	±0.03	3.7	433	238	2.504	503	143	2.793	168	172	3.074	118	186
2.5	±0.03	3.6	423	232	2.609	493	140	2.923	162	170	3.230	113	183
2.6	±0.03	3.6	413	227	2.714	482	137	3.053	156	167	3.387	108	181
2.7	±0.03	3.5	403	222	2.818	472	134	3.183	150	165	3.543	103	178
2.8	±0.03	3.4	395	217	2.933	463	133	3.336	147	164	3.742	100	177
2.9	±0.03	3.4	388	213	3.047	453	131	3.489	144	162	3.940	97	175
3	±0.03	3.3	380	209	3.162	444	130	3.642	140	161	4.139	95	174
3.1	±0.05	3.2	372	205	3.276	435	129	3.795	137	160	4.337	92	172
3.2	±0.05	3.2	365	201	3.391	425	127	3.947	134	159	4.536	89	171
3.3	±0.05	3.1	357	196	3.506	416	126	4.100	131	157	4.734	86	169
3.4	±0.05	3.1	349	192	3.620	407	125	4.253	128	156	4.933	84	168
3.5	±0.05	3.1	342	188	3.735	397	123	4.406	125	155	5.131	81	166
3.6	±0.05	3.0	334	184	3.849	388	122	4.559	121	154	5.330	78	165
3.7	±0.05	3.0	326	179	3.964	379	121	4.712	118	152	5.528	75	164
3.8	±0.05	3.0	318	175	4.078	369	119	4.865	115	151	5.727	73	162

Capacitance @ 1MHz and Tolerance		Self Resonance Frequency (GHz) Typ.	Q Standard Value @ 1GHz		Frequency 900MHz			Frequency 1900MHz			Frequency 2400MHz		
C (pF)	Tol.		Typ.	Min.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.
3.9	±0.05	2.9	311	171	4.193	360	118	5.018	112	150	5.925	70	161
4	±0.05	2.9	307	169	4.301	355	117	5.188	110	149	6.188	68	160
4.1	±0.05	2.8	303	167	4.410	351	116	5.358	108	148	6.450	67	159
4.2	±0.05	2.8	299	164	4.518	347	116	5.528	106	148	6.713	65	158
4.3	±0.05	2.7	295	162	4.627	342	115	5.698	104	147	6.975	64	157
4.4	±0.05	2.7	291	160	4.735	338	114	5.867	102	146	7.238	62	157
4.5	±0.05	2.7	287	158	4.843	333	113	6.037	100	146	7.500	61	156
4.6	±0.05	2.6	283	156	4.952	329	112	6.207	98	145	7.763	59	155
4.7	±0.05	2.6	279	154	5.060	324	112	6.377	96	144	8.025	58	154
5.1	±0.05	2.5	263	145	5.494	307	109	7.057	88	142	9.075	52	151
5.6	±0.05	2.4	244	134	6.035	285	105	7.906	78	138	10.39	44	147
6.2	±0.1	2.3	228	126	6.865	267	102	9.517	72	133	13.66	40	141
6.8	±0.1	2.2	213	117	7.694	250	100	11.13	66	128	16.93	35	135
7.5	±0.1	2.1	195	107	8.367	227	98	12.63	57	125	20.91	28	132
8.2	±0.1	2.0	176	97	9.041	205	96	14.14	49	123	24.88	21	129
9.1	±0.1	1.9	161	89	10.20	188	96	18.09	42	122	40.00	16	128
10	±1%	1.8	146	80	11.37	171	95	22.05	36	121	70.00	12	127
11	±1%	1.7	129	71	12.66	153	95	26.44	29	120	140.0	6	126
12	±1%	1.6	112	62	13.95	134	94	30.83	22	119	231.3	1	125
13	±1%	1.6	102	56	15.31	122	93	40.37	18	118	n/a	n/a	n/a
14	±1%	1.5	92	51	16.67	111	92	49.91	15	118	n/a	n/a	n/a
15	±1%	1.5	82	45	18.03	99	90	59.44	11	117	n/a	n/a	n/a
16	±1%	1.4	79	43	19.61	96	90	80.00	8	117	n/a	n/a	n/a
17	±1%	1.4	76	42	21.18	92	90	120.0	6	116	n/a	n/a	n/a
18	±1%	1.3	73	40	22.76	89	90	190.0	4	116	n/a	n/a	n/a
19	±1%	1.3	69	38	24.37	84	89	n/a	n/a	n/a	n/a	n/a	n/a
20	±1%	1.2	65	36	25.98	80	89	n/a	n/a	n/a	n/a	n/a	n/a
22	±1%	1.2	57	31	29.21	72	87	n/a	n/a	n/a	n/a	n/a	n/a
24	±1%	1.2	48	26	34.44	62	87	n/a	n/a	n/a	n/a	n/a	n/a
27	±1%	1.1	43	24	41.87	56	86	n/a	n/a	n/a	n/a	n/a	n/a
30	±1%	1.0	37	21	49.29	49	85	n/a	n/a	n/a	n/a	n/a	n/a
33	±1%	1.0	32	18	56.72	43	84	n/a	n/a	n/a	n/a	n/a	n/a
36	±1%	1.0	27	15	64.15	37	83	n/a	n/a	n/a	n/a	n/a	n/a
39	±1%	1.0	21	12	71.57	30	82	n/a	n/a	n/a	n/a	n/a	n/a

Accu-P® Series

0805 Typical Electrical Tables

Capacitance @ 1MHz and Tolerance		Self Resonance Frequency (GHz) Typ.	Q Standard Value @ 1GHz		Frequency 900MHz			Frequency 1900MHz			Frequency 2400MHz		
C (pF)	Tol.		Typ.	Min.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.
0.1	±0.02	17.2	880	484	0.125	890	3296	0.125	545	2417	0.126	447	2265
0.15	±0.02	14.1	872	480	0.176	885	2073	0.178	530	1626	0.181	434	1546
0.2	±0.02	12.3	864	475	0.228	880	1492	0.231	516	1227	0.235	420	1178
0.25	±0.02	11.0	857	471	0.279	874	1156	0.284	501	986	0.290	407	955
0.3	±0.02	10.1	849	467	0.331	869	938	0.337	487	825	0.344	394	804
0.35	±0.02	9.4	841	462	0.382	864	787	0.390	472	710	0.399	380	695
0.4	±0.02	8.8	833	458	0.433	859	675	0.443	458	623	0.453	367	613
0.45	±0.02	8.3	825	454	0.485	853	590	0.496	443	555	0.508	353	549
0.5	±0.02	7.9	817	450	0.536	848	523	0.549	429	501	0.562	340	497
0.55	±0.02	7.5	811	446	0.584	843	469	0.600	420	456	0.616	331	454
0.6	±0.02	7.2	805	443	0.631	838	425	0.651	411	419	0.670	322	418
0.65	±0.02	6.9	798	439	0.679	834	387	0.702	402	387	0.724	313	388
0.7	±0.02	6.7	792	436	0.726	829	356	0.753	393	360	0.778	304	362
0.75	±0.02	6.5	786	432	0.774	824	329	0.804	384	337	0.832	295	339
0.8	±0.02	6.3	779	429	0.822	819	306	0.855	375	316	0.886	286	319
0.85	±0.02	6.1	773	425	0.869	814	285	0.906	366	298	0.940	277	301
0.9	±0.02	5.9	767	422	0.917	810	267	0.957	357	282	0.994	268	285
0.95	±0.02	5.8	760	418	0.964	805	251	1.008	348	267	1.049	260	271
1	±0.02	5.6	754	415	1.012	800	231	1.059	339	235	1.103	251	242
1.05	±0.02	5.5	747	411	1.065	794	223	1.120	335	228	1.170	247	235
1.1	±0.02	5.4	740	407	1.119	788	215	1.181	330	221	1.237	244	228
1.15	±0.02	5.3	732	403	1.172	782	208	1.242	326	214	1.304	240	220
1.2	±0.02	5.1	725	399	1.225	776	200	1.304	322	207	1.371	237	213
1.25	±0.02	5.0	718	395	1.279	770	192	1.365	318	200	1.438	233	206
1.3	±0.02	4.9	711	391	1.332	764	184	1.426	313	193	1.505	230	199
1.35	±0.02	4.9	704	387	1.386	758	176	1.487	309	186	1.573	226	192
1.4	±0.02	4.8	696	383	1.439	752	169	1.548	305	179	1.640	223	184
1.45	±0.02	4.7	689	379	1.492	746	161	1.609	300	172	1.707	219	177
1.5	±0.02	4.6	682	375	1.546	740	153	1.670	296	165	1.774	216	170
1.55	±0.02	4.6	675	371	1.600	733	151	1.734	292	163	1.850	212	168
1.6	±0.02	4.5	668	367	1.654	726	148	1.799	287	161	1.927	208	165
1.65	±0.02	4.4	660	363	1.708	719	146	1.864	283	159	2.003	204	163
1.7	±0.02	4.3	653	359	1.762	712	143	1.928	278	157	2.079	200	160
1.75	±0.02	4.3	646	355	1.816	705	141	1.993	274	155	2.156	197	158
1.8	±0.02	4.2	639	351	1.870	698	139	2.058	269	152	2.232	193	155
1.85	±0.02	4.2	632	347	1.924	691	136	2.122	265	150	2.308	189	153
1.9	±0.02	4.1	624	343	1.978	684	134	2.187	260	148	2.385	185	150
1.95	±0.02	4.1	617	339	2.033	677	131	2.252	256	146	2.461	181	148
2	±0.03	4.0	610	336	2.087	670	129	2.316	251	144	2.537	177	145
2.1	±0.03	3.9	597	328	2.183	658	127	2.440	245	142	2.690	171	143
2.2	±0.03	3.8	584	321	2.280	646	124	2.563	239	139	2.843	165	141
2.3	±0.03	3.8	571	314	2.377	634	122	2.687	233	137	2.996	159	139
2.4	±0.03	3.6	557	307	2.474	623	119	2.810	227	135	3.149	154	136
2.5	±0.03	3.6	544	299	2.571	611	117	2.934	221	133	3.301	148	134
2.6	±0.03	3.6	531	292	2.668	599	114	3.057	215	130	3.454	142	132
2.7	±0.03	3.4	518	285	2.764	587	112	3.181	209	128	3.607	136	130
2.8	±0.03	3.4	507	279	2.875	575	111	3.348	204	127	3.850	132	129
2.9	±0.03	3.4	497	273	2.987	564	110	3.514	199	125	4.093	129	127
3	±0.03	3.3	486	267	3.098	552	109	3.681	194	124	4.335	125	126
3.1	±0.05	3.3	475	261	3.209	540	108	3.848	189	123	4.578	121	125
3.2	±0.05	3.2	465	256	3.320	528	107	4.014	183	122	4.821	118	123
3.3	±0.05	3.1	454	250	3.431	517	106	4.181	178	120	5.064	114	122
3.4	±0.05	3.1	443	244	3.542	505	105	4.348	173	119	5.307	110	121
3.5	±0.05	3.1	433	238	3.653	493	104	4.515	168	118	5.549	107	119
3.6	±0.05	3.0	422	232	3.764	481	103	4.681	163	116	5.792	103	118
3.7	±0.05	3.0	412	226	3.875	470	102	4.848	158	115	6.035	99	116
3.8	±0.05	3.0	401	220	3.986	458	101	5.015	153	114	6.278	96	115
3.9	±0.05	2.9	390	215	4.097	446	100	5.182	148	113	6.521	92	114
4	±0.05	2.9	384	211	4.214	440	99	5.378	144	112	6.861	89	113
4.1	±0.05	2.9	378	208	4.331	434	98	5.574	141	112	7.201	86	113
4.2	±0.05	2.8	372	205	4.448	428	98	5.769	138	111	7.541	84	112

Capacitance @ 1MHz and Tolerance		Self Resonance Frequency (GHz) Typ.	Q Standard Value @ 1GHz		Frequency 900MHz			Frequency 1900MHz			Frequency 2400MHz		
C (pF)	Tol.		Typ.	Min.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.
4.3	±0.05	2.7	366	202	4.564	422	97	5.965	134	111	7.881	81	111
4.4	±0.05	2.7	360	198	4.681	415	96	6.161	131	110	8.222	78	111
4.5	±0.05	2.7	355	195	4.798	409	96	6.357	128	110	8.562	75	110
4.6	±0.05	2.7	349	192	4.915	403	95	6.553	124	109	8.902	72	110
4.7	±0.05	2.6	343	188	5.032	397	94	6.749	121	109	9.242	69	109
5.1	±0.05	2.5	319	175	5.499	373	91	7.533	108	107	10.60	58	107
5.6	±0.05	2.4	289	159	6.083	342	88	8.513	91	104	12.30	44	104
6.2	±0.1	2.3	264	145	6.842	313	86	10.43	79	102	18.03	36	103
6.8	±0.1	2.2	239	131	7.601	283	84	12.35	68	101	23.76	28	102
7.5	±0.1	2.1	218	120	8.468	259	83	14.84	61	100	37.25	21	101
8.2	±0.1	2.0	198	109	9.334	234	82	17.32	55	100	50.74	15	100
9.1	±0.1	1.9	179	99	10.57	213	82	24.90	46	100	n/a	n/a	n/a
10	±1%	1.8	160	88	11.80	191	81	32.48	37	100	n/a	n/a	n/a
11	±1%	1.7	139	77	13.17	167	81	40.90	26	101	n/a	n/a	n/a
12	±1%	1.6	119	65	14.54	143	80	49.32	16	101	n/a	n/a	n/a
13	±1%	1.6	110	60	16.17	134	80	n/a	n/a	n/a	n/a	n/a	n/a
14	±1%	1.5	101	55	17.79	125	80	n/a	n/a	n/a	n/a	n/a	n/a
15	±1%	1.5	92	51	19.42	116	80	n/a	n/a	n/a	n/a	n/a	n/a
16	±1%	1.4	87	48	21.13	110	79	n/a	n/a	n/a	n/a	n/a	n/a
17	±1%	1.4	83	46	22.85	104	78	n/a	n/a	n/a	n/a	n/a	n/a
18	±1%	1.3	78	43	24.57	99	77	n/a	n/a	n/a	n/a	n/a	n/a
19	±1%	1.3	73	40	26.41	92	77	n/a	n/a	n/a	n/a	n/a	n/a
20	±1%	1.3	67	37	28.26	85	76	n/a	n/a	n/a	n/a	n/a	n/a
22	±1%	1.2	57	31	31.95	72	76	n/a	n/a	n/a	n/a	n/a	n/a
24	±1%	1.2	46	25	35.64	59	75	n/a	n/a	n/a	n/a	n/a	n/a
27	±1%	1.1	41	22	44.94	54	74	n/a	n/a	n/a	n/a	n/a	n/a
30	±1%	1.0	36	20	54.24	48	73	n/a	n/a	n/a	n/a	n/a	n/a
33	±1%	1.0	30	17	63.54	42	72	n/a	n/a	n/a	n/a	n/a	n/a
36	±1%	0.9	25	14	72.84	37	71	n/a	n/a	n/a	n/a	n/a	n/a
39	±1%	0.9	20	11	82.14	31	70	n/a	n/a	n/a	n/a	n/a	n/a
43	±1%	0.9	16	9	102.9	27	66	n/a	n/a	n/a	n/a	n/a	n/a
47	±1%	0.8	12	7	123.7	23	63	n/a	n/a	n/a	n/a	n/a	n/a

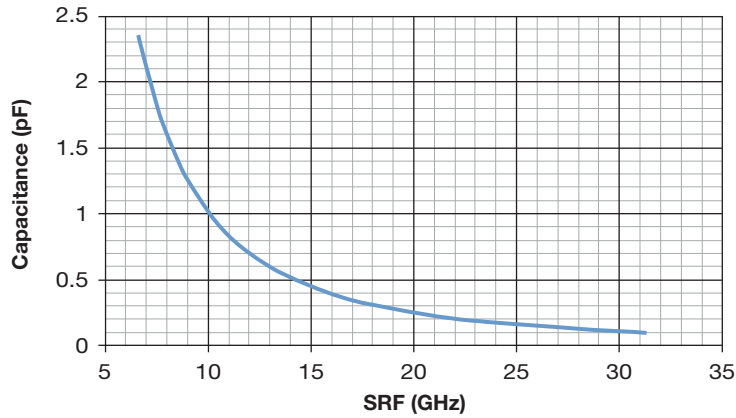
Accu-P® Series

1210 Typical Electrical Tables

Capacitance @ 1MHz and Tolerance		Self Resonance Frequency (GHz) Typ.	Q Standard Value @ 1GHz		Frequency 900MHz			Frequency 1900MHz			Frequency 2400MHz		
C (pF)	Tol.		Typ.	Min.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.
0.1	±0.02	15.6	1190	654	0.136	1176	3633	0.136	606	2149	0.136	450	2068
0.15	±0.03	12.7	1179	648	0.190	1166	2129	0.190	597	1407	0.191	444	1370
0.2	±0.02	11.0	1168	642	0.244	1156	1457	0.244	589	1042	0.246	438	1023
0.25	±0.02	9.8	1156	636	0.297	1145	1086	0.299	581	826	0.301	432	816
0.3	±0.02	8.9	1145	630	0.351	1135	854	0.353	573	683	0.356	426	678
0.35	±0.02	8.3	1134	624	0.405	1125	697	0.408	565	581	0.411	421	580
0.4	±0.02	7.7	1123	618	0.459	1115	584	0.462	557	505	0.466	415	506
0.45	±0.02	7.3	1112	612	0.513	1105	500	0.516	549	447	0.521	409	449
0.5	±0.02	6.9	1101	606	0.567	1095	435	0.571	541	400	0.576	403	404
0.55	±0.02	6.6	1090	599	0.617	1084	384	0.621	532	362	0.627	397	366
0.6	±0.02	6.3	1079	593	0.666	1074	342	0.672	524	331	0.679	391	335
0.65	±0.02	6.0	1068	587	0.716	1064	308	0.723	516	304	0.731	385	309
0.7	±0.02	5.8	1057	581	0.765	1054	279	0.774	508	282	0.783	379	287
0.75	±0.02	5.6	1046	575	0.815	1044	255	0.824	500	262	0.834	374	267
0.8	±0.02	5.4	1035	569	0.864	1034	234	0.875	492	245	0.886	368	250
0.85	±0.02	5.3	1023	563	0.914	1024	216	0.926	484	230	0.938	362	236
0.9	±0.02	5.1	1012	557	0.963	1013	201	0.976	476	217	0.989	356	222
0.95	±0.02	5.0	1001	551	1.013	1003	187	1.027	467	205	1.041	350	210
1	±0.02	5.0	992	546	1.062	983	167	1.078	459	170	1.093	344	177
1.05	±0.02	4.9	981	539	1.107	975	163	1.124	451	167	1.141	338	174
1.1	±0.02	4.8	969	533	1.152	966	158	1.170	443	165	1.189	331	172
1.15	±0.02	4.7	958	527	1.196	958	154	1.217	435	162	1.236	325	169
1.2	±0.02	4.6	946	521	1.241	950	150	1.263	427	160	1.284	318	167
1.25	±0.02	4.5	935	514	1.285	942	146	1.309	419	157	1.332	312	164
1.3	±0.02	4.4	923	508	1.330	933	142	1.355	410	155	1.380	305	162
1.35	±0.02	4.3	912	502	1.375	925	138	1.402	402	152	1.428	299	159
1.4	±0.02	4.2	900	495	1.419	917	134	1.448	394	150	1.476	293	156
1.45	±0.02	4.1	889	489	1.464	908	129	1.494	386	147	1.524	286	154
1.5	±0.02	4.1	877	483	1.508	900	125	1.541	378	144	1.572	280	151
1.55	±0.02	4.0	862	474	1.567	890	123	1.618	371	143	1.638	274	150
1.6	±0.02	3.9	846	465	1.626	881	122	1.694	363	142	1.704	268	149
1.65	±0.02	3.9	831	457	1.685	871	120	1.771	356	140	1.770	262	148
1.7	±0.02	3.8	815	448	1.743	862	118	1.848	349	139	1.836	256	147
1.75	±0.02	3.7	800	440	1.802	852	116	1.925	342	138	1.902	250	145
1.8	±0.02	3.7	784	431	1.861	843	114	2.002	334	136	1.968	244	144
1.85	±0.02	3.6	769	423	1.920	833	112	2.079	327	135	2.034	239	143
1.9	±0.02	3.5	753	414	1.978	824	110	2.156	320	134	2.100	233	142
1.95	±0.02	3.4	737	406	2.037	814	108	2.233	313	132	2.167	227	141
2	±0.03	3.3	722	397	2.096	805	107	2.310	305	131	2.233	221	139
2.1	±0.03	3.2	691	380	2.213	786	103	2.464	291	128	2.365	209	137
2.2	±0.03	3.0	660	363	2.331	767	99	2.618	276	126	2.497	198	135
2.3	±0.03	2.9	644	354	2.420	747	97	2.681	268	123	2.613	191	132
2.4	±0.03	2.9	629	346	2.508	728	96	2.744	259	121	2.729	185	130
2.5	±0.03	2.8	614	338	2.597	709	94	2.807	251	118	2.845	179	128
2.6	±0.03	2.8	598	329	2.686	689	93	2.870	242	116	2.961	173	126
2.7	±0.03	2.7	583	321	2.775	670	91	2.933	234	114	3.077	167	123
2.8	±0.03	2.7	574	316	2.875	659	90	3.047	230	113	3.205	164	122
2.9	±0.03	2.7	566	311	2.975	647	89	3.162	227	112	3.334	161	121
3	±0.03	2.7	557	306	3.075	636	88	3.276	223	111	3.462	157	121
3.1	±0.05	2.7	548	302	3.174	625	87	3.390	220	110	3.590	154	120
3.2	±0.05	2.6	540	297	3.274	613	87	3.504	216	109	3.718	151	119
3.3	±0.05	2.6	531	292	3.374	602	86	3.619	213	108	3.847	148	118
3.4	±0.05	2.6	522	287	3.474	591	85	3.733	209	107	3.975	145	117
3.5	±0.05	2.6	514	283	3.574	579	84	3.847	206	106	4.103	141	116
3.6	±0.05	2.5	505	278	3.674	568	83	3.961	202	105	4.231	138	115
3.7	±0.05	2.5	496	273	3.773	556	82	4.076	198	104	4.359	135	114
3.8	±0.05	2.5	488	268	3.873	545	81	4.190	195	103	4.488	132	113

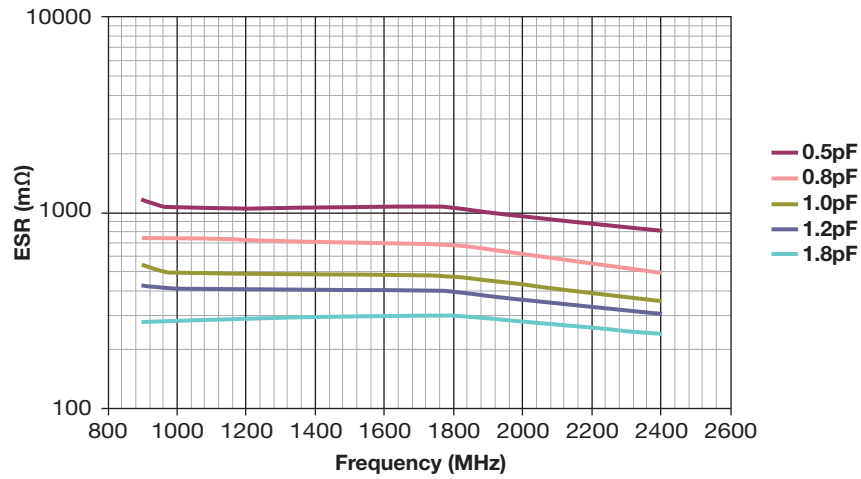
Capacitance @ 1MHz and Tolerance		Self Resonance Frequency (GHz) Typ.	Q Standard Value @ 1GHz		Frequency 900MHz			Frequency 1900MHz			Frequency 2400MHz		
C (pF)	Tol.		Typ.	Min.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.	C(eff) (pF) Typ.	Q Typ.	ESR (mOhm) Typ.
3.9	±0.05	2.4	479	264	3.973	534	80	4.304	191	102	4.616	129	112
4	±0.05	2.4	473	260	4.083	528	79	4.435	189	101	4.768	127	112
4.1	±0.05	2.4	467	257	4.192	522	78	4.565	186	100	4.919	125	111
4.2	±0.05	2.4	462	254	4.302	516	78	4.695	183	100	5.071	123	110
4.3	±0.05	2.3	456	251	4.411	511	77	4.825	180	99	5.223	121	110
4.4	±0.05	2.3	450	247	4.521	505	76	4.956	178	98	5.375	119	109
4.5	±0.05	2.3	444	244	4.630	499	75	5.086	175	98	5.526	117	108
4.6	±0.05	2.3	438	241	4.740	493	75	5.216	172	97	5.678	115	108
4.7	±0.05	2.2	432	238	4.849	487	74	5.347	170	96	5.830	113	107
5.1	±0.05	2.1	408	225	5.288	464	71	5.868	159	93	6.437	106	105
5.6	±0.05	2.0	379	208	5.835	435	67	6.519	145	90	7.195	96	102
6.2	±0.1	1.9	355	195	6.440	408	65	7.176	137	86	7.897	91	96
6.8	±0.1	1.8	330	182	7.044	380	62	7.832	129	83	8.599	85	91
7.5	±0.1	1.7	308	169	7.823	351	61	8.927	115	81	10.08	74	89
8.2	±0.1	1.7	285	157	8.601	322	60	10.02	100	78	11.55	63	87
9.1	±0.1	1.6	266	146	9.600	304	58	11.55	93	77	13.93	57	85
10	±1%	1.5	247	136	10.60	285	57	13.09	85	76	16.30	50	84
11	±1%	1.5	225	124	11.71	265	56	14.79	76	74	18.94	43	82
12	±1%	1.4	204	112	12.82	244	54	16.49	68	73	21.57	36	81
13	±1%	1.3	193	106	13.97	230	53	18.64	61	72	26.09	32	80
14	±1%	1.3	181	99	15.13	215	53	20.80	55	71	30.61	28	79
15	±1%	1.2	169	93	16.28	200	52	22.95	48	70	35.13	24	78
16	±1%	1.2	164	90	17.51	195	51	26.01	46	69	46.51	22	76
17	±1%	1.2	159	88	18.75	189	50	29.07	43	67	57.90	19	75
18	±1%	1.1	154	85	19.98	183	49	32.14	41	66	69.29	17	73
19	±1%	1.1	150	82	21.21	178	49	36.34	39	66	n/a	n/a	n/a
20	±1%	1.1	145	80	22.43	172	49	40.55	38	65	n/a	n/a	n/a
22	±1%	1.0	136	75	24.88	162	49	48.96	34	64	n/a	n/a	n/a
24	±1%	1.0	126	70	27.34	151	48	57.38	31	63	n/a	n/a	n/a
27	±1%	0.9	112	62	31.02	135	48	70.00	26	62	n/a	n/a	n/a
30	±1%	0.9	101	56	36.14	121	48	n/a	n/a	n/a	n/a	n/a	n/a
33	±1%	0.8	90	50	41.27	108	48	n/a	n/a	n/a	n/a	n/a	n/a
36	±1%	0.8	79	44	46.39	95	48	n/a	n/a	n/a	n/a	n/a	n/a
39	±1%	0.8	68	38	51.52	82	48	n/a	n/a	n/a	n/a	n/a	n/a
43	±1%	0.7	54	30	58.35	64	48	n/a	n/a	n/a	n/a	n/a	n/a
47	±1%	0.7	39	21	65.18	46	48	n/a	n/a	n/a	n/a	n/a	n/a
82	±1%	0.7	17	10	148.400	24	48	n/a	n/a	n/a	n/a	n/a	n/a

Accu-P® 01005 Typical SRF vs Capacitance



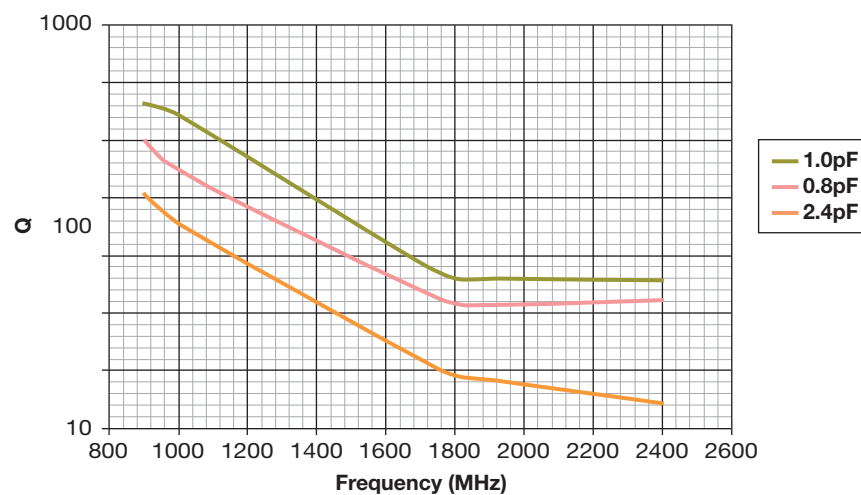
Measured on HP8720ES

Accu-P® 01005 Typical ESR vs Frequency

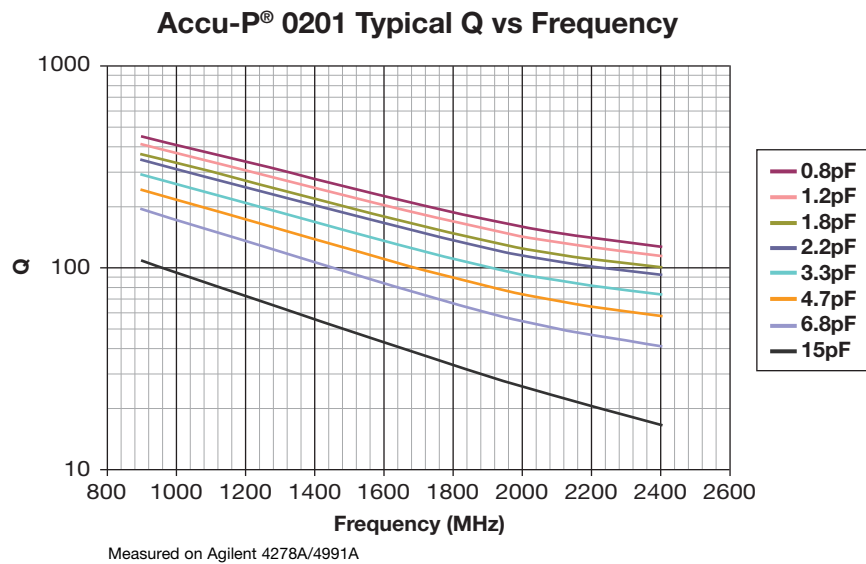
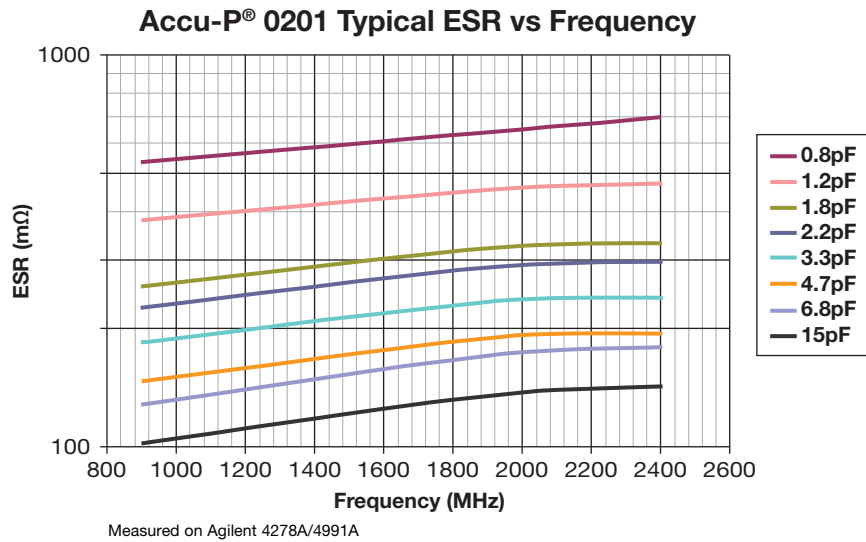
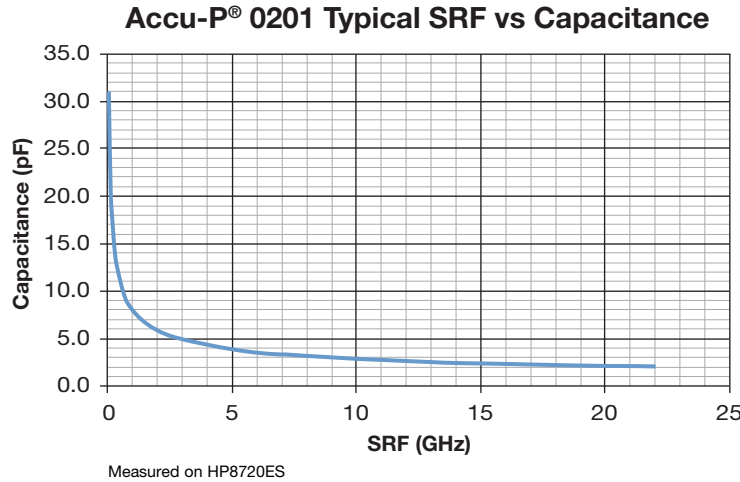


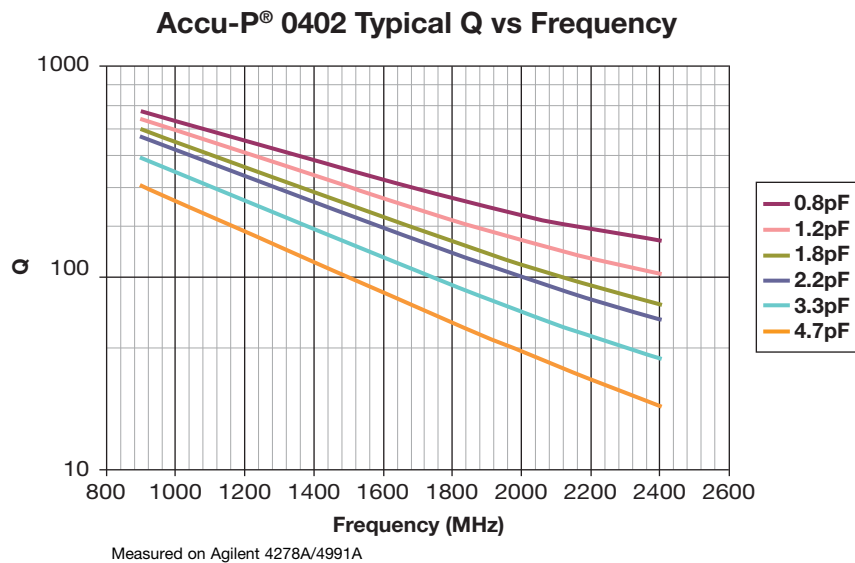
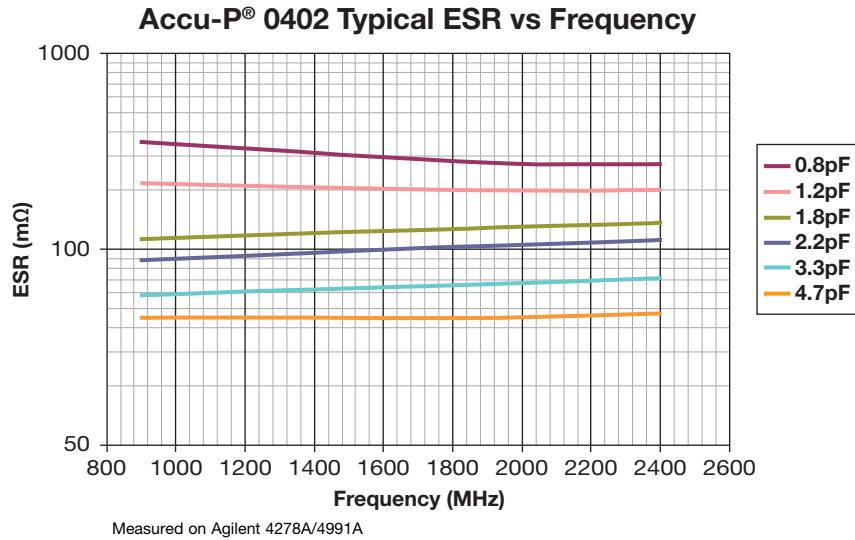
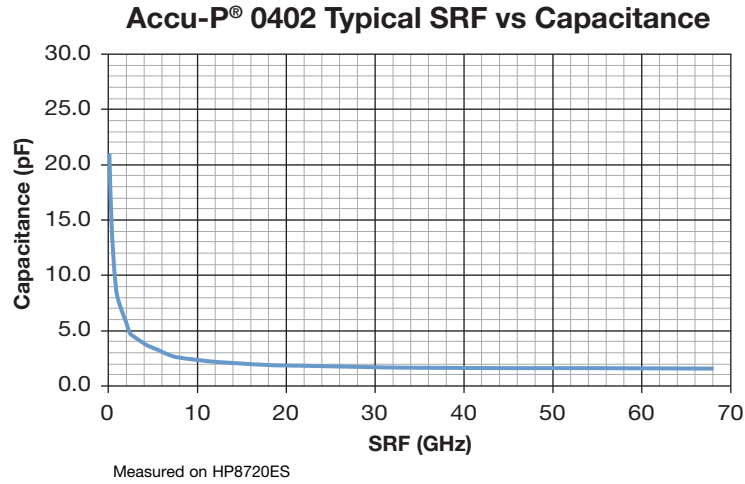
Measured on Agilent 4278A/4991A

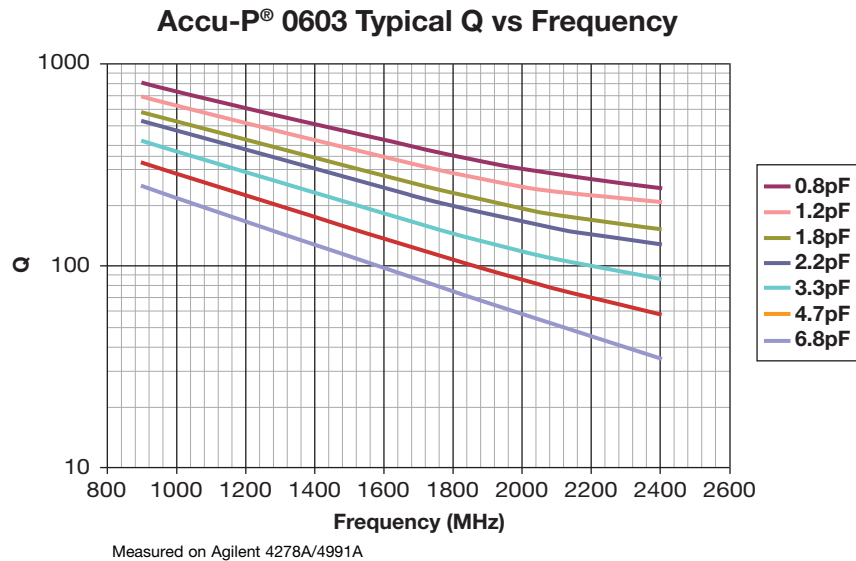
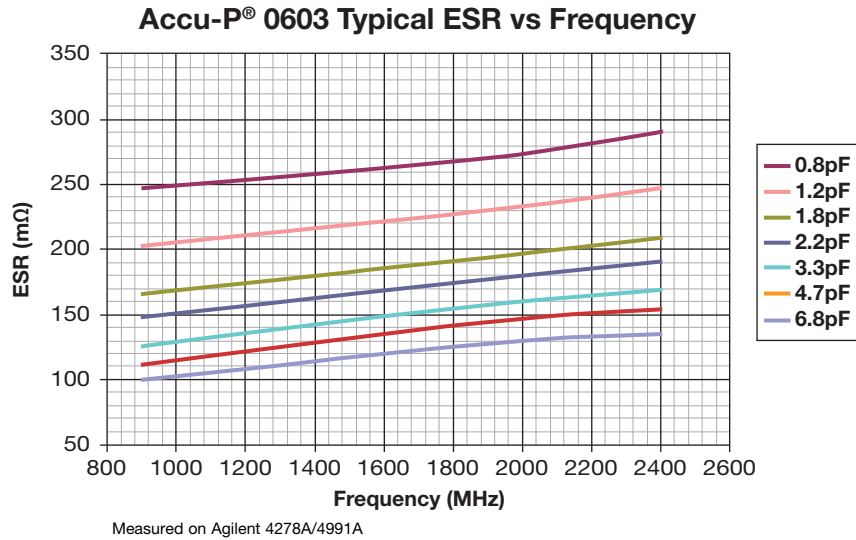
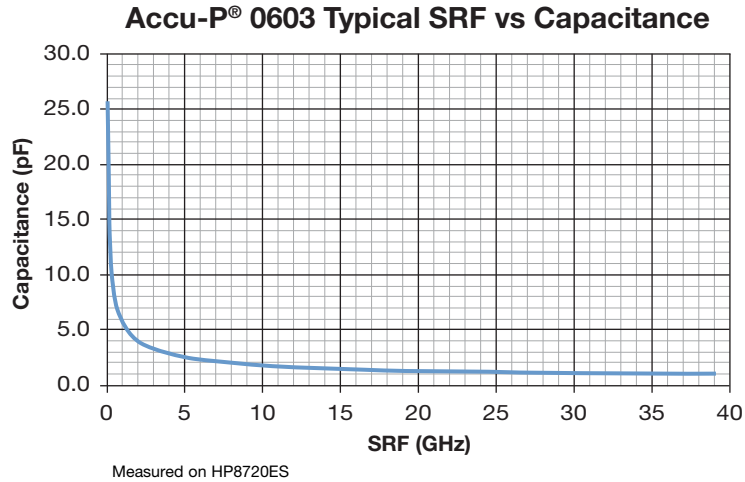
Accu-P® 01005 Typical Q vs Frequency

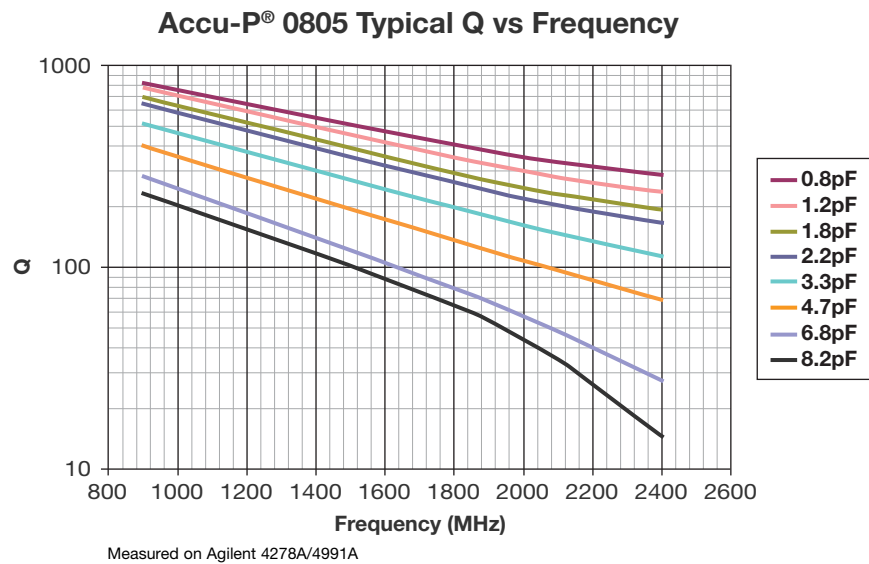
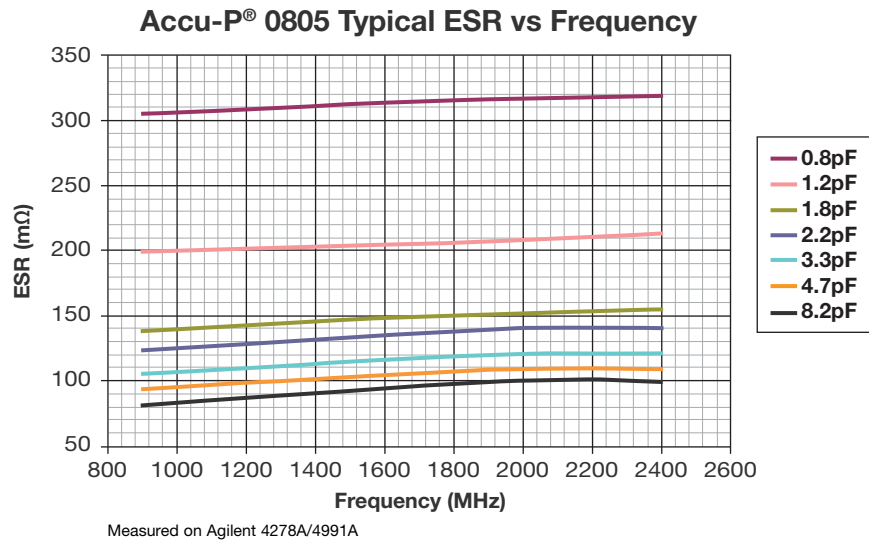
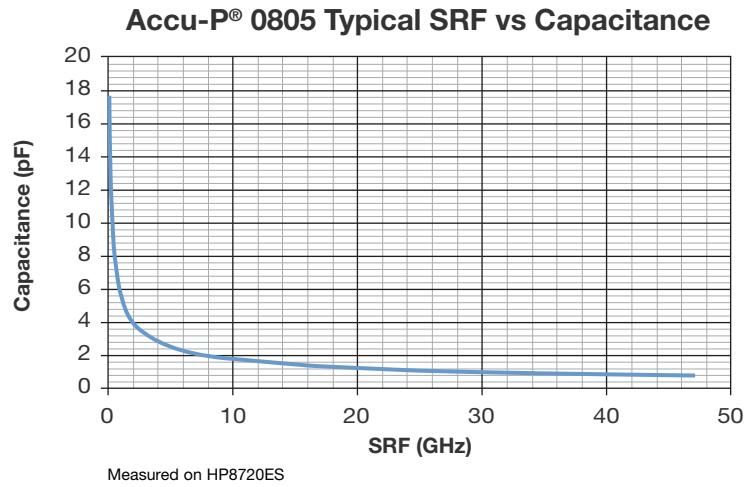


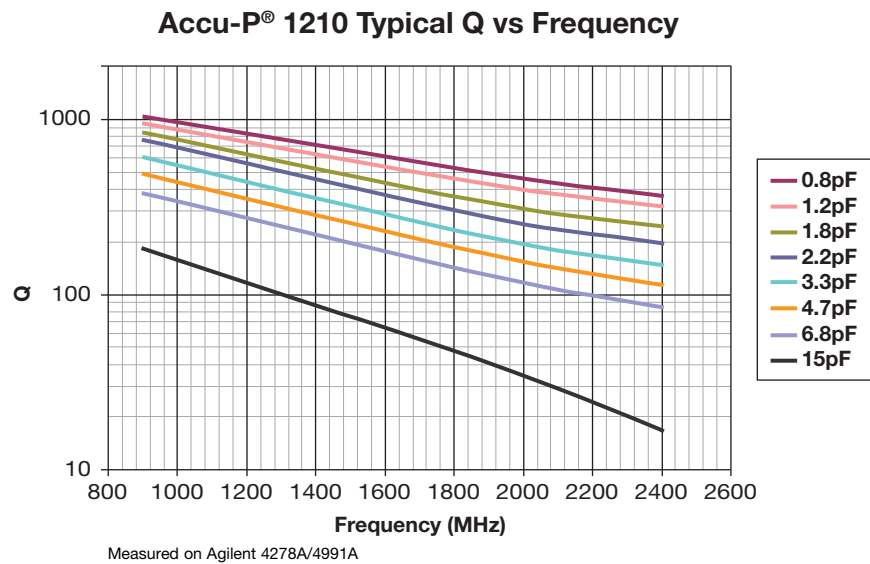
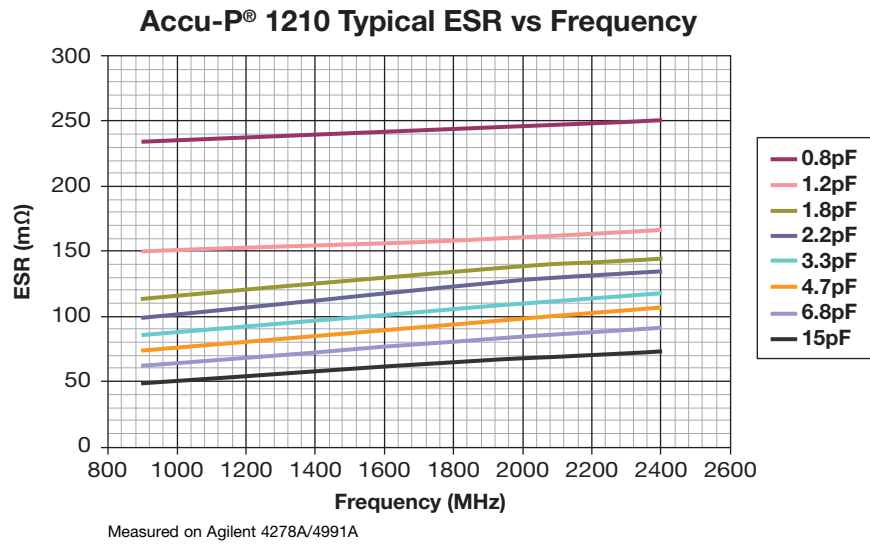
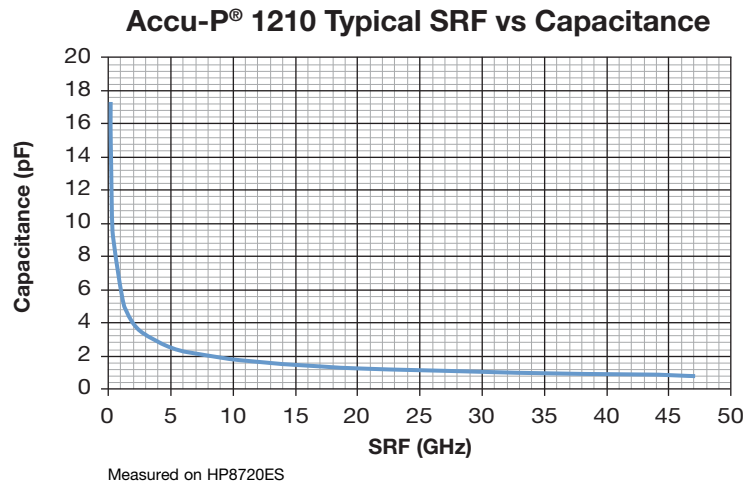
Measured on Agilent 4278A/4991A







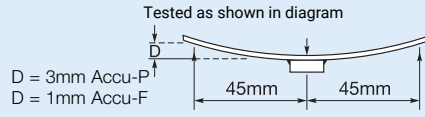




ENVIRONMENTAL CHARACTERISTICS

TEST	CONDITIONS	REQUIREMENT
Life (Endurance) MIL-STD-202F Method 108A	125°C, 2UR, 1000 hours	No visible damage $\Delta C/C \leq 2\%$ for $C \geq 5pF$ $\Delta C \leq 0.25pF$ for $C < 5pF$
Accelerated Damp Heat Steady State MIL-STD-202F Method 103B	85°C, 85% RH, UR, 1000 hours	No visible damage $\Delta C/C \leq 2\%$ for $C \geq 5pF$ $\Delta C \leq 0.25pF$ for $C < 5pF$
Temperature Cycling MIL-STD-202F Method 107E MIL-STD-883D Method 1010.7	-55°C to +125°C, 15 cycles – Accu-P®	No visible damage $\Delta C/C \leq 2\%$ for $C \geq 5pF$ $\Delta C \leq 0.25pF$ for $C < 5pF$
Resistance to Solder Heat IEC-68-2-58	260°C ± 5°C for 10 secs	C remains within initial limits

MECHANICAL CHARACTERISTICS

TEST	CONDITIONS	REQUIREMENT
Solderability IEC-68-2-58	Components completely immersed in a solder bath at 235°C for 2 secs.	Terminations to be well tinned, minimum 95% coverage
Leach Resistance IEC-68-2-58	Components completely immersed in a solder bath at 260±5°C for 60 secs.	Dissolution of termination faces ≤15% of area Dissolution of termination edges ≤25% of length
Adhesion MIL-STD-202F Method 211A	A force of 5N applied for 10 secs.	No visible damage
Termination Bond Strength IEC-68-2-21 Amend. 2	Tested as shown in diagram 	No visible damage $\Delta C/C \leq 2\%$ for $C \geq 5pF$ $\Delta C \leq 0.25pF$ for $C < 5pF$
Robustness of Termination IEC-68-2-21 Amend. 2	A force of 5N applied for 10 secs.	No visible damage
High Frequency Vibration MIL-STD-202F Method 201A, 204D (Accu-P® only)	55Hz to 2000Hz, 20G	No visible damage
Storage	12 months minimum with components stored in "as received" packaging	Good solderability

QUALITY & RELIABILITY

Accu-P® is based on well established thin-film technology and materials.

• ON-LINE PROCESS CONTROL

This program forms an integral part of the production cycle and acts as a feedback system to regulate and control production processes. The test procedures, which are integrated into the production process, were developed after long research work and are based on the highly developed semiconductor industry test procedures and equipment. These measures help KYOCERA AVX to produce a consistent and high yield line of products.

• FINAL QUALITY INSPECTION

Finished parts are tested for standard electrical parameters and visual/mechanical characteristics. Each production lot is 100% evaluated for: capacitance and proof voltage at 2.5 UR. In addition, production is periodically evaluated for:

- Average capacitance with histogram printout for capacitance distribution;
- IR and Breakdown Voltage distribution;
- Temperature Coefficient;
- Solderability;
- Dimensional, mechanical and temperature stability.

QUALITY ASSURANCE

The reliability of these thin-film chip capacitors has been studied intensively for several years. Various measures have been taken to obtain the high reliability required today by the industry. Quality assurance policy is based on well established international industry standards. The reliability of the capacitors is determined by accelerated testing under the following conditions:

Life (Endurance)	125°C, 2UR, 1000 hours
Accelerated Damp Heat Steady State	85°C, 85% RH, UR, 1000 hours.

Accu-P® Series

Performance Characteristics RF Power Applications

RF POWER APPLICATIONS

In RF power applications capacitor losses generate heat. Two factors of particular importance to designers are:

- Minimizing the generation of heat.
- Dissipating heat as efficiently as possible.

CAPACITOR HEATING

- The major source of heat generation in a capacitor in RF power applications is a function of RF current (I) and ESR, from the relationship:
- Power dissipation = $I_{RMS}^2 \times ESR$
- Accu-P® capacitors are specially designed to minimize ESR and therefore RF heating. Values of ESR for Accu-P® capacitors are significantly less than those of ceramic MLC components currently available.

HEAT DISSIPATION

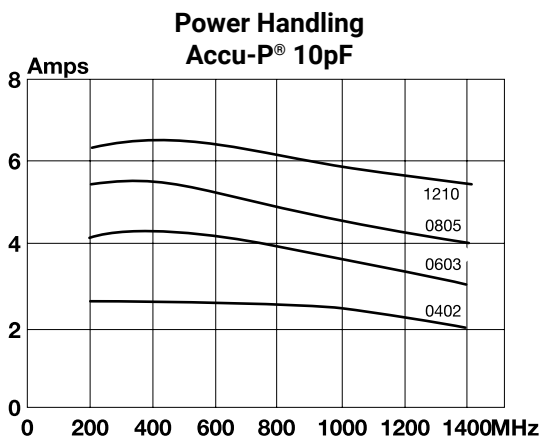
- Heat is dissipated from a capacitor through a variety of paths, but the key factor in the removal of heat is the thermal conductivity of the capacitor material.
- The higher the thermal conductivity of the capacitor, the more rapidly heat will be dissipated.
- The table below illustrates the importance of thermal conductivity to the performance of Accu-P® in power applications.

Data used in calculating the graph:

Thermal impedance of capacitors:

- 0402 17°C/W
- 0603 12°C/W
- 0805 6.5°C/W
- 1210 5°C/W

PRODUCT	MATERIAL	THERMAL CONDUCTIVITY W/mK
Accu-P® Microwave MLC	Alumina Magnesium Titanate	18.9 6.0



Thermal impedance measured using RF generator, amplifier and strip-line transformer. ESR of capacitors measured on Boonton 34A

THERMAL IMPEDANCE

Thermal impedance of Accu-P® chips is shown below compared with the thermal impedance of Microwave MLC's.

The thermal impedance expresses the temperature difference in °C between chip center and termination caused by a power dissipation of 1 watt in the chip. It is expressed in °C/W.

ADVANTAGES OF ACCU-P® IN RF POWER CIRCUITS

The optimized design of Accu-P® offers the designer of RF power circuits the following advantages:

- Reduced power losses due to the inherently low ESR of Accu-P®.
- Increased power dissipation due to the high thermal conductivity of Accu-P®.
- The only true test of a capacitor in any particular application is its performance under operating conditions in the actual circuit.

CAPACITOR TYPE	CHIP SIZE	THERMAL IMPEDANCE (°C/W)
Accu-P®	0805	6.5
	1210	5
Microwave MLC	0505	12
	1210	7.5

PRACTICAL APPLICATION IN RF POWER CIRCUITS

- There is a wide variety of different experimental methods for measuring the power handling performance of a capacitor in RF power circuits. Each method has its own problems and few of them exactly reproduce the conditions present in "real" circuit applications.
- Similarly, there is a very wide range of different circuit applications, all with their unique characteristics and operating conditions which cannot possibly be covered by such "theoretical" testing.

GENERAL

Accu-P® SMD capacitors are designed for soldering to printed circuit boards or other substrates. The construction of the components is such that they will withstand the time/temperature profiles used in both wave and reflow soldering methods.

CIRCUIT BOARD TYPE

The circuit board types which may be used with Accu-P® are as follows:

All flexible types of circuit boards
(eg. FR-4, G-10) and also alumina.

For other circuit board materials, please consult factory.

HANDLING

SMD capacitors should be handled with care to avoid damage or contamination from perspiration and skin oils. The use of plastic tipped tweezers or vacuum pick-ups is strongly recommended for individual components. Bulk handling should ensure that abrasion and mechanical shock are minimized. For automatic equipment, taped and reeled product gives the ideal medium for direct presentation to the placement machine.

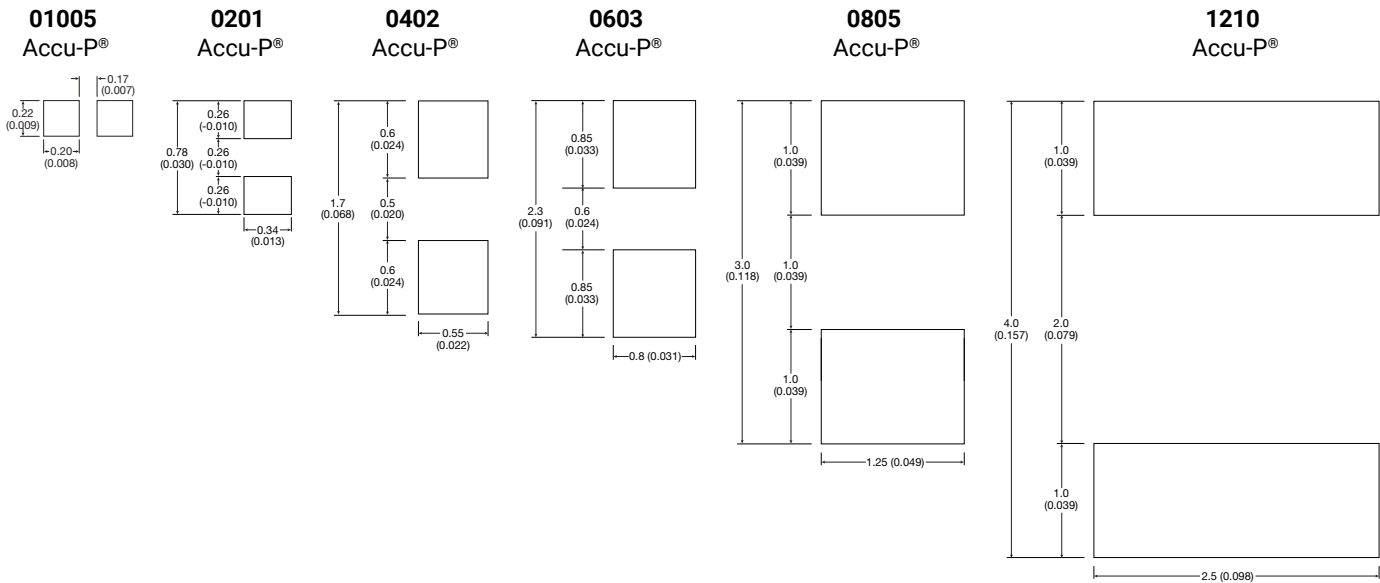
COMPONENT PAD DESIGN

Component pads must be designed to achieve good joints and minimize component movement during reflow soldering. Pad designs are given below for both wave and reflow soldering.

The basis of these designs is:

- Pad width equal to component width. It is permissible to decrease this to as low as 85% of component width but it is not advisable to go below this.
- Pad overlap 0.5mm beneath large components. Pad overlap about 0.3mm beneath small components.
- Pad extension of 0.5mm for reflow of large components and pad extension about 0.3mm for reflow of small components. Pad extension about 1.0mm for wave soldering.

REFLOW SOLDERING PAD DIMENSIONS: millimeters (inches)



PREHEAT & SOLDERING

The rate of preheat in production should not exceed 4°C/ second and a recommended maximum is about 2°C/second. Temperature differential from preheat to soldering should not exceed 100°C.

For further specific application or process advice, please consult KYOCERA AVX.

COOLING

After soldering, the assembly should preferably be allowed to cool naturally. In the event of assisted cooling, similar conditions to those recommended for preheating should be used.

HAND SOLDERING & REWORK

Hand soldering is permissible. Preheat of the PCB to 150°C is required. The most preferable technique is to use hot air soldering tools. Where a soldering iron is used, a temperature controlled model not exceeding 30 watts should be used and set to not more than 260°C.

CLEANING RECOMMENDATIONS

Care should be taken to ensure that the devices are thoroughly cleaned of flux residues, especially the space beneath the device. Such residues may otherwise become conductive and effectively offer a lossy bypass to the device. Various recommended cleaning conditions (which must be optimized for the flux system being used) are as follows:

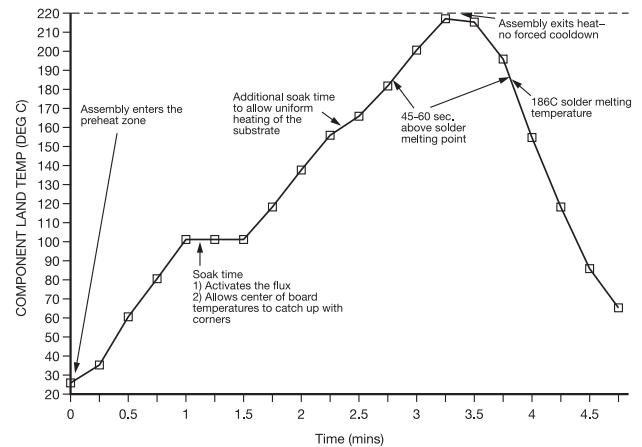
- Cleaning liquids.....i-propanol, ethanol, acetylacetone, water and other standard PCB cleaning liquids.
- Ultrasonic conditions.....power-20w/liter max. frequency-20kHz to 45kHz.
- Temperature80°C maximum (if not otherwise limited by chosen solvent system).
- Time.....5 minutes max.

STORAGE CONDITIONS

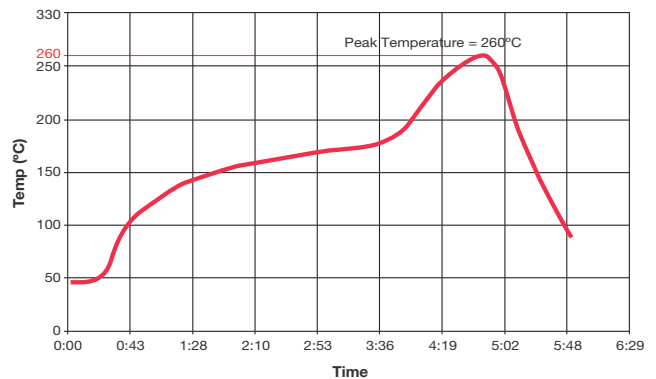
Recommended storage conditions for Accu-P® prior to use are as follows:

- Temperature15°C to 35°C
- Humidity ≤65%
- Air Pressure860mbar to 1060mbar

RECOMMENDED REFLOW SOLDERING PROFILE COMPONENTS WITH SNPB TERMINATIONS



RECOMMENDED REFLOW SOLDERING PROFILE LEAD FREE COMPONENTS WITH SN100 TERMINATIONS



TAPE & REEL

All tape and reel specifications are in compliance with EIA 481-1-A. (equivalent to IEC 286 part 3).

- 8mm carrier
- Reeled quantities: Reels of 3,000 per 7" reel or 10,000 pieces per 13" reel
01005, 0201, and 0402 = 5,000 pieces per 7" reel and 20,000 pieces per 13" reel

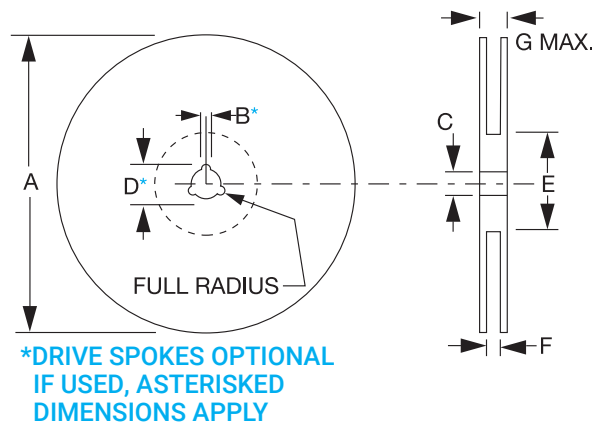
REEL DIMENSIONS: millimeters (inches)

A(1)	B	C	D	E	F	G
180±1.0 (7.087±0.039)	1.5 min. (0.059 min.)	13±0.2 (0.512 ± 0.008)	20.2 min. (0.795 min.)	50 min. (1.969 min.)	9.6±1.5 (0.370 ± 0.050)	14.4 max. (0.567 max.)

Metric dimensions will govern.

Inch measurements rounded and for reference only.

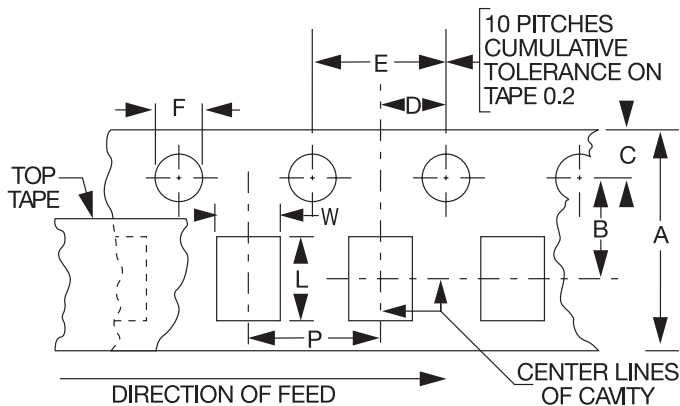
(1) 330mm (13 inch) reels are available.



CARRIER DIMENSIONS: millimeters (inches)

A	B	C	D	E	F
8.0 ± 0.3 (0.315 ± 0.012)	3.5 ± 0.05 (0.138 ± 0.002)	1.75 ± 0.1 (0.069 ± 0.004)	2.0 ± 0.05 (0.079 ± 0.002)	4.0 ± 0.1 (0.157 ± 0.004)	1.5 ^{+0.1} _{-0.0} (0.059 ^{+0.004} _{-0.000})

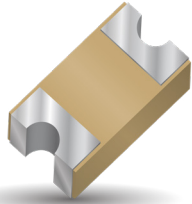
The nominal dimensions of the component compartment (W,L) are derived from the component size.



P = 4mm for 0603, 0805, 1210
P = 2mm for 01005, 0201, and 0402

Multilayer Organic (MLO®) Inductors

Hi-Q



The Multilayer Organic Hi-Q Inductor is a low profile organic based inductor that can support mobile communications, satellite applications, GPS, matching networks, and collision avoidance. The MLO® Hi-Q Inductor series of components are based on KYOCERA AVX patented multilayer organic technology (US patent 6,987,307 and 7,439,840). MLO® Hi-Q Inductors incorporate very low loss organic materials and low profile copper which allow for high Q and high stability over frequency. MLO® Hi-Q Inductors are surface mountable and are expansion matched to FR4 printed wiring boards. MLO® Hi-Q Inductors utilize fine line high density interconnect technology thereby allowing for tight tolerance control and high repeatability. Reliability testing is performed to JEDEC and mil standards. Finishes are available in RoHS compliant Sn.

APPLICATIONS

- Mobile communications
- Satellite Applications
- GPS
- Collision Avoidance
- Wireless LAN's

FEATURES

- High Q
- High SRF
- High Frequency
- Low DC Resistance
- Surface Mountable
- 0402 Case Size
- RoHS Compliant Finishes
- Available in Tape and Reel

SURFACE MOUNT ADVANTAGES

- Inherent Low Profile
- Excellent Solderability
- Low Parasitics
- Better Heat Dissipation
- Expansion Matched to PCB

HOW TO ORDER

HLQ
Type
HLQ = High Q

02
Size
02 = 0402

XXX
Inductance
Expressed in nH
(2 significant digits + number of zeros)
for values <10nH,
letter R denotes decimal point.
Example:
22nH = 220
4.7nH = 4R7

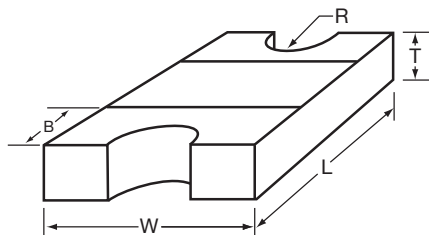
X
Tolerance
B = ±0.1nH
C = ±0.2nH
H = ±3%

T
Termination
Sn100

TR
Packaging
5000pcs T&R



DIMENSIONS



mm (inches)

L	W	T	R	B
1.00±0.10 (0.040±0.004)	0.58±0.075 (0.023±0.003)	0.35±0.10 (0.014±0.004)	0.125±0.050 (0.005±0.002)	0.23±0.0508 (0.0092±0.002)

QUALITY INSPECTION

Finished parts are 100% tested for electrical parameters and visual characteristics.

TERMINATION

RoHS compliant Sn finish.

OPERATING TEMPERATURE

-55°C to +125°C

Multilayer Organic (MLO®) Inductors

Hi-Q

0402 ELECTRICAL SPECIFICATIONS

L (nH) 450MHz	Available Inductance Tolerance B = ±0.1nH, C = ±0.2nH H = ±3%	Q min 450MHz	SRF min (GHz)	Rdc max (mΩ)	Idc max (mA)
0.8	±0.1nH, ±0.2nH	17	7	100	350
0.9	±0.1nH, ±0.2nH	17	7	100	350
1	±0.1nH, ±0.2nH	17	7	100	330
1.1	±0.1nH, ±0.2nH	17	7	100	330
1.2	±0.1nH, ±0.2nH	17	7	110	330
1.3	±0.1nH, ±0.2nH	17	7	130	330
1.5	±0.1nH, ±0.2nH	17	7	150	330
1.6	±0.1nH, ±0.2nH	17	7	150	300
1.8	±0.1nH, ±0.2nH	17	7	160	300
2	±0.1nH, ±0.2nH	17	7	180	245
2.2	±0.1nH, ±0.2nH	17	7	200	245
2.4	±0.1nH, ±0.2nH	17	7	200	245
2.7	±0.1nH, ±0.2nH	17	7	250	245
3	±0.1nH, ±0.2nH	17	7	300	225
3.3	±0.1nH, ±0.2nH	17	7	340	225
3.6	±0.1nH, ±0.2nH	17	7	350	200
3.9	±0.1nH, ±0.2nH	17	7	400	200
4.7	±0.1nH, ±0.2nH	17	7	480	195
5.6	±0.1nH, ±0.2nH	17	7	500	170
6.8	±3%	17	7	600	160
8.2	±3%	17	6	800	130
10	±3%	17	5	1000	120
12	±3%	17	4	1100	110
15	±3%	17	4	1200	110
18	±3%	17	3	1500	110
22	±3%	17	3	1900	95
27	±3%	17	3	2100	95
30	±3%	17	2	2200	85
32	±3%	17	2	2200	85

Specifications based on performance of component assembled properly on printed circuit board with 50Ω nominal impedance.
Idc max: Maximum 15°C rise in component temperature over ambient.

Mouser Electronics

Authorized Distributor

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KYOCERA AVX:

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[HLQ024R7CTTR](#)

Features

- Low Series Resistance
- Ultra Low Capacitance
- Millimeter Wave Switching & Cutoff Frequency
- Useable up to 70 GHz
- 2 Nanosecond Switching Speed
- Can be Driven by a Buffered TTL
- Silicon Nitride Passivation
- Polyimide Scratch Protection
- RoHS Compliant

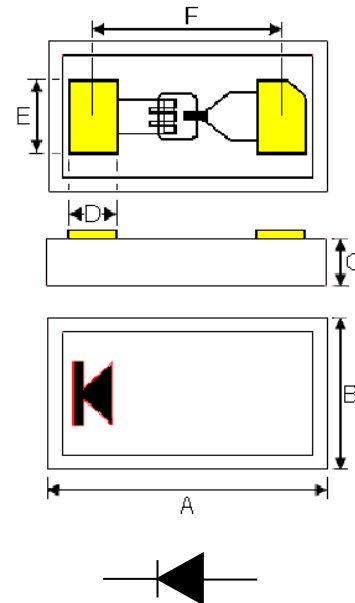
Description

The MADP-000907-14020 is a solderable, flip-chip Aluminum Gallium Arsenide (AlGaAs) PIN diode. It is fabricated with MOCVD grown epitaxy using a process and design that optimizes device to device uniformity and produces extremely low parasitics. The diode exhibits an exceptionally low RC product (0.1 ps) and a 2-3 ns switching speed. The chips are fully passivated with silicon nitride and have an added BCB polymer layer for scratch protection. The BCB protective coating prevents damage to the diode junction area and anode air-bridge during handling and assembly.

The ultra low capacitance of the MADP-000907-14020 allows for operation at millimeter wave frequencies for RF switches and phase shifter applications. The diode is designed to be used in pulsed or CW applications, where single digit ns switching speed is required. The low capacitance of the PIN diode makes it ideal for use in many microwave multi-throw switch assemblies, where the series capacitance of each “off” port adversely loads the input and affects VSWR.

Ordering Information

Part Number	Package
MADP-000907-14020W	Waffle Pack
MADP-000907-14020P	Tape and Reel



1. Backside metal: 0.2 μm gold over 4 μm nickel.
2. Yellow hatched areas indicate backside ohmic gold contacts.

Outline Dimension

DIM	INCHES		MM	
	Min.	Max.	Min.	Max.
A	0.029	0.030	0.750	0.765
B	0.015	0.016	0.380	0.395
C	0.007	0.008	0.175	0.195
D	0.004	0.005	0.101	0.127
E	0.007	0.0073	0.177	0.185
F	0.018	0.019	0.457	0.482

* Restrictions on Hazardous Substances, European Union Directive 2011/65/EU.

Electrical Specifications: $T_A = +25^\circ\text{C}$

Symbol	Parameter	Conditions	Units	Min	Typ	Max
C_T	Total Capacitance	-10 V, 1 MHz	pF	—	0.025	0.030
R_S	Series Resistance	10 mA, 1 GHz	Ω	—	5.2	7.0
V_F	Forward Voltage	10 mA	V	—	1.33	1.45
I_R	Reverse Leakage Current ³	$V_R = -45\text{ V}$	nA	—	—	50
T_{RISE} / T_{FALL}	Switching Speed ⁴	10 GHz	ns	—	2	—

3. The max rated $V_R(-45\text{V})$ is sourced and the resultant reverse leakage current, I_R , is measured to be $<50\text{nA}$

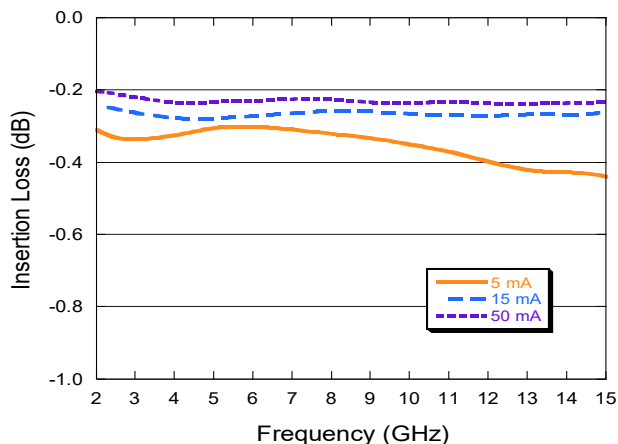
4. Switching speed is measured between 10% and 90% or 90% to 10% RF voltage for a single series mounted diode. Driver delay is not included.

Absolute Maximum Ratings: $T_A = 25^\circ\text{C}$

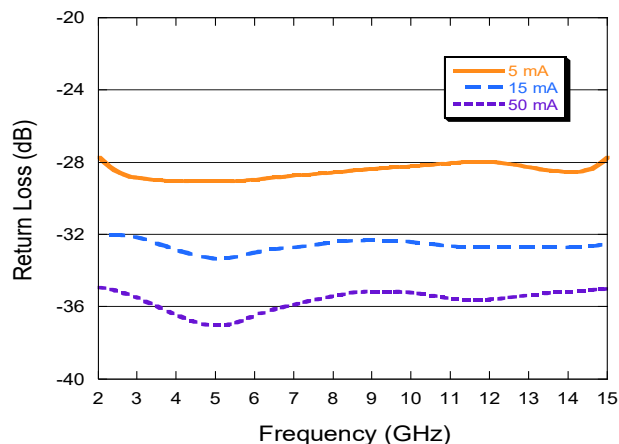
Parameter	Absolute Maximum
Reverse Voltage	45V
Operating Temperature	-55°C to $+125^\circ\text{C}$
Storage Temperature	-55°C to $+150^\circ\text{C}$
Junction Temperature	$+175^\circ\text{C}$
Dissipated Power (RF + DC)	100mW
C.W. Incident Power	+23 dBm
Mounting Temperature	$+280^\circ\text{C}$ for 10 seconds

Typical Performance Curves

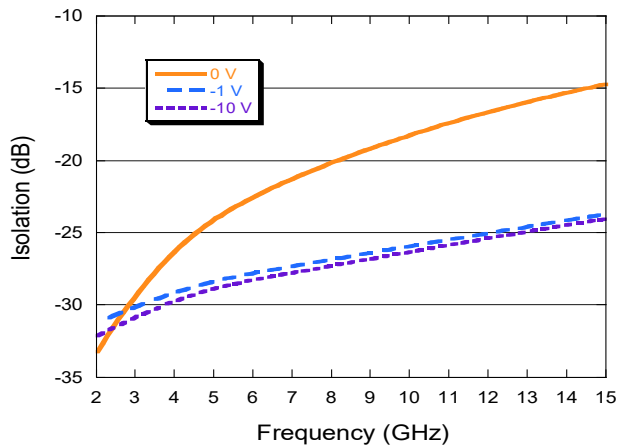
Insertion Loss vs. Frequency



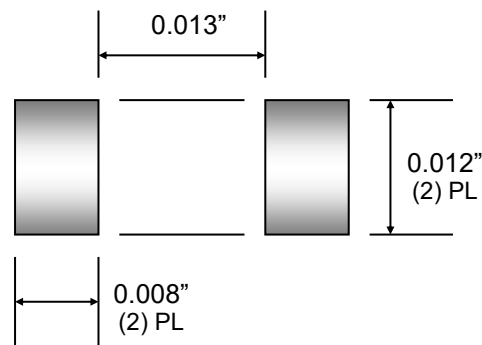
Return Loss vs. Frequency



Isolation vs. Frequency



Circuit Pad Layout



Device Installation Guidelines

Cleanliness

This device should be handled in a clean environment. The chip is resistant to solvents and may be cleaned using approved industry standard practices and chemicals.

Static Sensitivity

Aluminum Gallium Arsenide PIN diodes are ESD sensitive and can be damaged by static electricity. Proper ESD handling techniques should be used. These devices are rated Class 1A, (0-250 V) HBM.

General Handling

The die has a BCB, polymer layer which provides scratch protection for the junction area and the anode air bridge. Die can be handled with plastic tweezers or picked and placed with a #27 tip vacuum pencil.

Assembly Requirements using Electrically Conductive Silver Epoxy

The MADP-000907-14020 is designed to be inserted onto hard or soft substrates with the junction/pad side down. It may be mounted onto a silk-screened circuit using electrically conductive silver epoxy, approximately 1-2 mils in thickness and cured at approximately 90°C to 150°C per manufacturer's schedule. For extended cure times, >30 minutes, temperatures must be kept below 200°C.

Eutectic Solder Die Attached

63/37 Sn/Pb or any RoHS compliant solder may be used for diode attachment. It is recommended that the attachment surface be preheated to 100°C prior to re-flow in order to minimize CTE mismatches. Gradual temperature ramp up and ramp down is also recommended with a maximum soldering temperature of 280°C for less than 10 seconds. See **Application Note [M538](#)** for recommended soldering profile.

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RO4000® Series

High Frequency Circuit Materials

RO4000® hydrocarbon ceramic laminates are designed to offer superior high frequency performance and low cost circuit fabrication. The result is a low loss material which can be fabricated using standard epoxy/glass (FR-4) processes offered at competitive prices.

The selection of laminates typically available to designers is significantly reduced once operational frequencies increase to 500 MHz and above. RO4000 material possesses the properties needed by designers of RF microwave circuits and matching networks and controlled impedance transmission lines. Low dielectric loss allows RO4000 series material to be used in many applications where higher operating frequencies limit the use of conventional circuit board laminates. The temperature coefficient of dielectric constant is among the lowest of any circuit board material (Chart 1), and the dielectric constant is stable over a broad frequency range (Chart 2). For reduced insertion loss, LoPro® foil is available (Chart 3). This makes it an ideal substrate for broadband applications.

RO4000 material's thermal coefficient of expansion (CTE) provides several key benefits to the circuit designer. The expansion coefficient of RO4000 material is similar to that of copper which allows the material to exhibit excellent dimensional stability, a property needed for mixed dielectric multi-layer boards constructions. The low Z-axis CTE of RO4000 laminates provides reliable plated through-hole quality, even in severe thermal shock applications. RO4000 series material has a Tg of >280°C (536°F) so its expansion characteristics remain stable over the entire range of circuit processing temperatures.

RO4000 series laminates can easily be fabricated into printed circuit boards using standard FR-4 circuit board processing techniques. Unlike PTFE based high performance materials, RO4000 series laminates do not require specialized via preparation processes such as sodium etch. This material is a rigid, thermoset laminate that is capable of being processed by automated handling systems and scrubbing equipment used for copper surface preparation.

RO4003C™ laminates are currently offered in various configurations utilizing both 1080 and 1674 glass fabric styles, with all configurations meeting the same laminate electrical performance specification. Specifically designed as a drop-in replacement for the RO4003C™ material, RO4350B™ laminates utilize RoHS compliant flame-retardant technology for applications requiring UL 94V-0 certification. These materials conform to the requirements of IPC-4103, slash sheet /10 for RO4003C, see note #1 for RO4350B slash sheet determination.



Data Sheet

FEATURES AND BENEFITS:

RO4000 materials are reinforced hydrocarbon/ceramic laminates - not PTFE

- Designed for performance sensitive, high volume applications

Low dielectric tolerance and low loss

- Excellent electrical performance
- Allows applications with higher operating frequencies
- Ideal for broadband applications

Stable electrical properties vs. frequency

- Controlled impedance transmission lines
- Repeatable design of filters

Low thermal coefficient of dielectric constant

- Excellent dimensional stability

Low Z-axis expansion

- Reliable plated through holes

Low in-plane expansion coefficient

- Remains stable over an entire range of circuit processing temperatures

Volume manufacturing process

- RO4000 laminates can be fabricated using standard glass epoxy processes
- Competitively priced

CAF resistant

SOME TYPICAL APPLICATIONS:

- Cellular Base Station Antennas and Power Amplifiers
- RF Identification Tags
- Automotive Radar and Sensors
- LNB's for Direct Broadcast Satellites



Chart 1: RO4000 Series Materials Dielectric Constant vs. Temperature

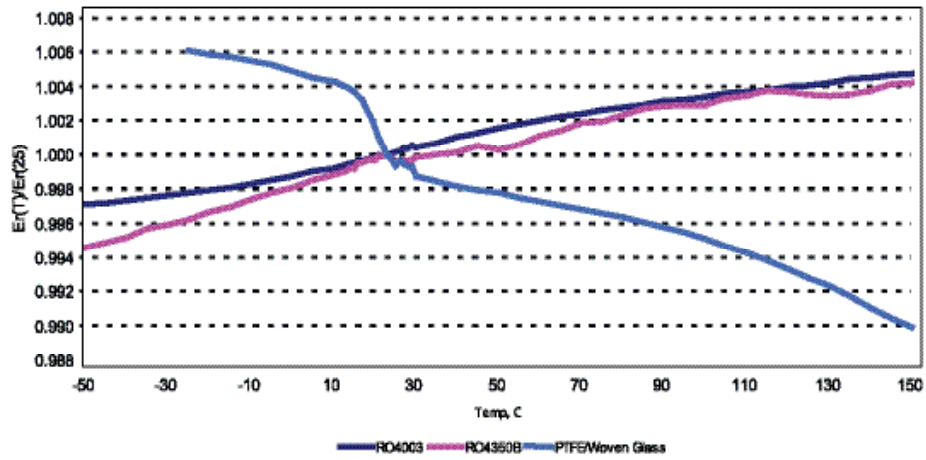


Chart 2: RO4000 Series Materials Dielectric Constant vs. Frequency

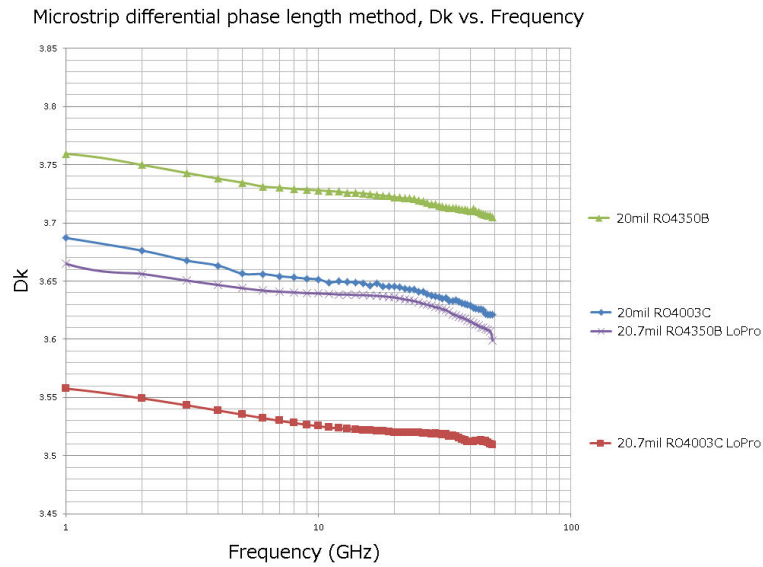
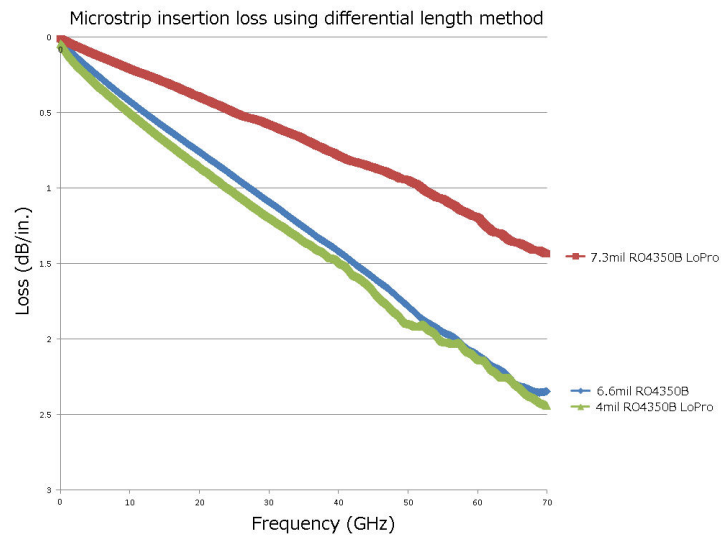


Chart 3: Microstrip Insertion Loss



Property	Typical Value		Direction	Units	Condition	Test Method
	RO4003C	RO4350B				
Dielectric Constant, ϵ_r Process	3.38 ± 0.05	⁽¹⁾ 3.48 ± 0.05	Z	-	10 GHz/23°C	IPC-TM-650 2.5.5.5 Clamped Stripline
⁽²⁾ Dielectric Constant, ϵ_r Design	3.55	3.66	Z	-	8 to 40 GHz	Differential Phase Length Method
Dissipation Factor tan, d	0.0027 0.0021	0.0037 0.0031	Z	-	10 GHz/23°C 2.5 GHz/23°C	IPC-TM-650 2.5.5.5
Thermal Coefficient of ϵ_r	+40	+50	Z	ppm/°C	-50°C to 150°C	IPC-TM-650 2.5.5.5
Volume Resistivity	1.7 X 10 ¹⁰	1.2 X 10 ¹⁰	-	MΩ·cm	COND A	IPC-TM-650 2.5.17.1
Surface Resistivity	4.2 X 10 ⁹	5.7 X 10 ⁹	-	MΩ	COND A	IPC-TM-650 2.5.17.1
Electrical Strength	31.2 (780)	31.2 (780)	Z	KV/mm (V/mil)	0.51mm (0.020")	IPC-TM-650 2.5.6.2
Tensile Modulus	19,650 (2,850) 19,450 (2,821)	16,767 (2,432) 14,153, (2,053)	X Y	MPa (ksi)	RT	ASTM D638
Tensile Strength	139 (20.2) 100 (14.5)	203 (29.5) 130 (18.9)	X Y	MPa (ksi)	RT	ASTM D638
Flexural Strength	276 (40)	255 (37)	-	MPa (kpsi)	-	IPC-TM-650 2.4.4
Dimensional Stability	<0.3	<0.5	X,Y	mm/m (mils/inch)	after etch +E2/150°C	IPC-TM-650 2.4.39A
Coefficient of Thermal Expansion	11 14 46	10 12 32	X Y Z	ppm/°C	-55 to 288°C	IPC-TM-650 2.4.41
Tg	>280	>280	-	°C TMA	A	IPC-TM-650 2.4.24.3
Td	425	390	-	°C TGA		ASTM D3850
Thermal Conductivity	0.71	0.69	-	W/m/°K	80°C	ASTM C518
Moisture Absorption	0.06	0.06	-	%	48 hrs immersion 0.060" sample Temperature 50°C	ASTM D570
Density	1.79	1.86	-	g/cm ³	23°C	ASTM D792
Copper Peel Strength	1.05 (6.0)	0.88 (5.0)	-	N/mm (pli)	after solder float 1 oz. EDC Foil	IPC-TM-650 2.4.8
Flammability	N/A	⁽³⁾ V-0	-	-	-	UL 94
Lead-Free Process Compatible	Yes	Yes	-	-	-	-

NOTES:

- (1) RO4350B 4 mil laminates have a process Dk of 3.33 ± 0.05 and are in conformance with IPC-4103A/240. All other RO4350B laminate thicknesses are /11 and /240 compliant.
- (2) The design Dk is an average number from several different tested lots of material and on the most common thickness/s. If more detailed information is required, please contact Rogers Corporation or refer to Rogers' technical papers in the Rogers Technology Support Hub available at <http://www.rogerscorp.com>.
- (3) RO4350B LoPro® laminates do not share the same UL designation as standard RO4350B laminates. A separate UL qualification may be necessary.

Typical values are a representation of an average value for the population of the property. For specification values contact Rogers Corporation.

RO4000 LoPro laminate uses a modified version of the RO4000 resin system to bond reverse treated foil. Values shown above are RO4000 laminates without the addition of the LoPro resin. LoPro foil results in an overall thickness increase of approximately 0.0007" (18µm) per core.

The LoPro Resin Dk is approximately 2.4. **However, when used in combination with the base laminate system, the average design Dk noted in the data sheet table should be used.** (The design Dk value decreases by about 0.1 as the core thickness decreases from 0.020" to 0.004")

Prolonged exposure in an oxidative environment may cause changes to the dielectric properties of hydrocarbon based materials. The rate of change increases at higher temperatures and is highly dependent on the circuit design. Although Rogers' high frequency materials have been used successfully in innumerable applications and reports of oxidation resulting in performance problems are extremely rare, Rogers recommends that the customer evaluate each material and design combination to determine fitness for use over the entire life of the end product.

Standard Thicknesses	Standard Panel Sizes	Standard Claddings
RO4003C: 0.008" (0.203mm) +/- 0.0010" 0.012" (0.305mm) +/- 0.0010" 0.016" (0.406mm) +/- 0.0015" 0.020" (0.508mm) +/- 0.0015" 0.032" (0.813mm) +/- 0.0020" 0.060" (1.524mm) +/- 0.0040" RO4350B: 0.004" (0.10mm) +/- 0.0007" 0.0066" (0.17mm) +/- 0.0007" 0.010" (0.25mm) +/- 0.0010" 0.020" (0.51mm) +/- 0.0015" 0.030" (0.76mm) +/- 0.0020" 0.060" (1.52mm) +/- 0.0040"	24" X 18" (610 X 457 mm) 24" X 21" (610 X 533 mm) 24" X 36" (610 X 915 mm) 48" X 36" (1219 X 915 mm) *Additional panel sizes available	<u>Electrodeposited Copper Foil</u> ½ oz. (18µm) HH/HH 1 oz. (35µm) H1/H1 *Additional cladding weights are available
*Additional non-standard thicknesses available from 0.0066" - 0.060" in varying increments		

*Contact Customer Service or Sales Engineering to inquire about additional available product configurations

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